

# Model Name: 8I945G-PRO Revision 1.0

SHEET TITLE

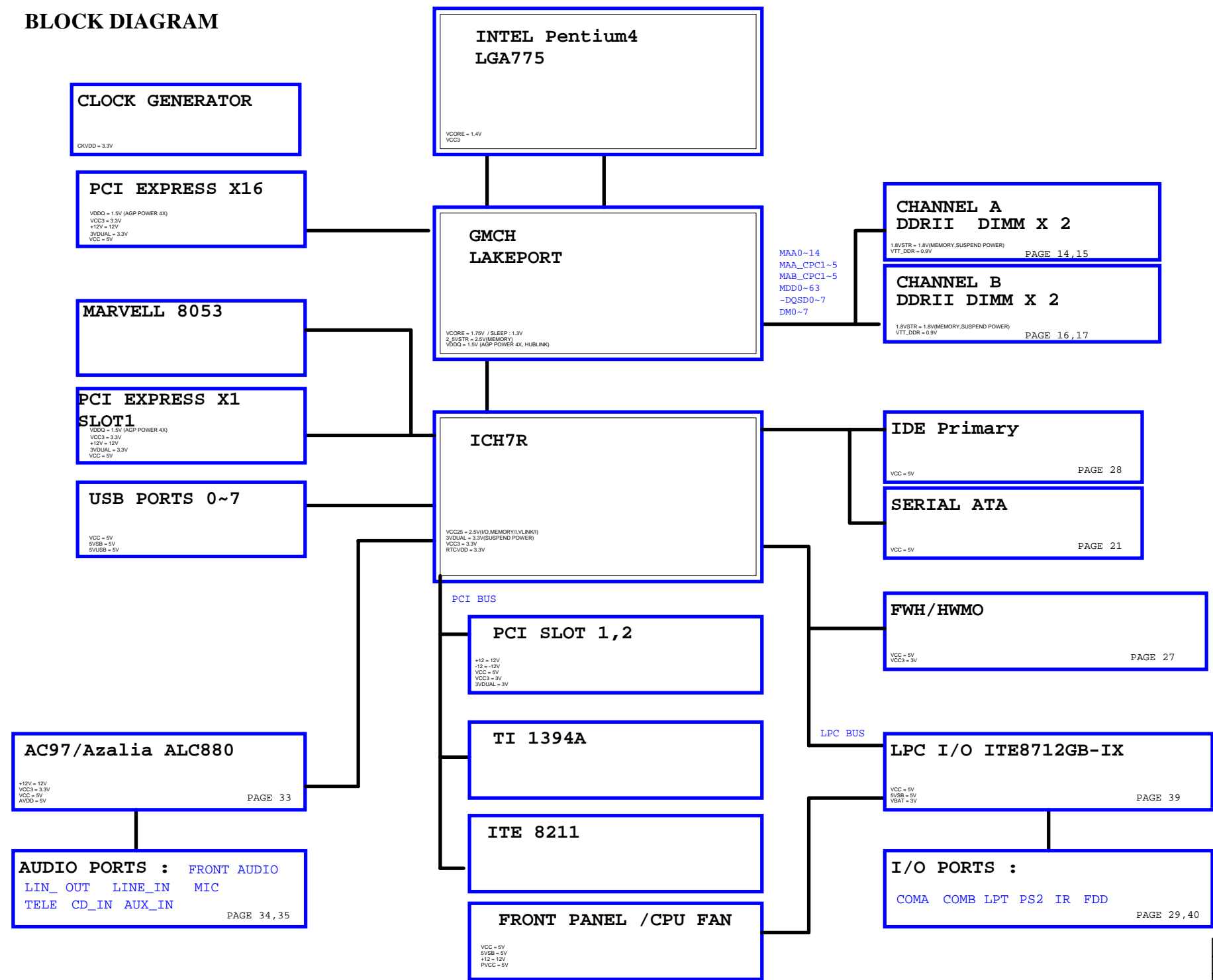
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B
06	P4_LGA775_C
07	P4_LGA775_D
08	GMCH-LAKEPORT_HOST
09	GMCH-LAKEPORT_DDRII
10	GMCH-LAKEPORT_PCI E, DMI
11	GMCH-LAKEPORT_INT VGA
12	GMCH-LAKEPORT_GND
13	GMCH-LAKEPORT_PWR
14	DDRII CHANNEL A 1,2
15	DDRII CHANNEL B 1,2
16	DDRII TERMINATION
17	PCI EXPRESS*16 SLOT
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
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22	ATX,ATX_12V CONNECT,DUAL BIOS
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25	H/W MONITOR,FAN
26	IDE
27	KB_PS2,S.P.R

SHEET TITLE

28	FRONT PANEL
29	FRONT USB,REAL USB CONNECT
30	PROCESSOR HOT
31	AZALIA CODEC ALC880
32	AUDIO JACK 1
33	AUDIO JACK 2
34	TI1394B
35	TI1394B
36	MARVELL 88E8053 LAN
37	ITE 8712GB
38	COM_LPT
39	VCORE PWM
40	DISCRETE POWER
41	ITE8212 RAID
42	ITE8212 RAID
43	GPIO DEFINE
44	GPIO DEFINE

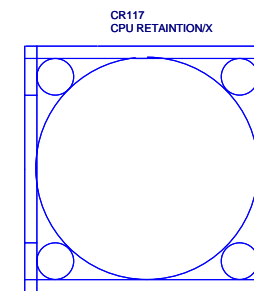
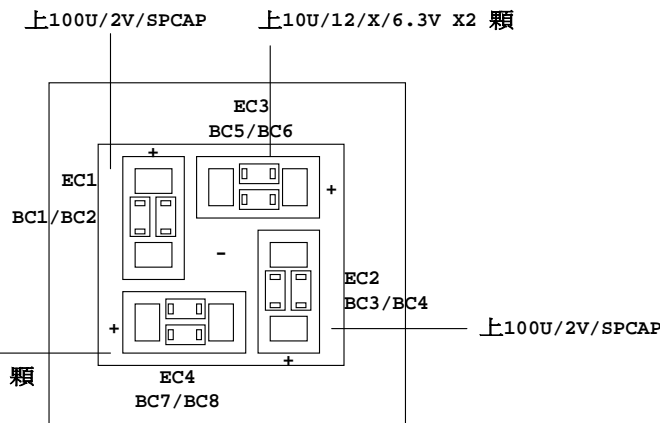
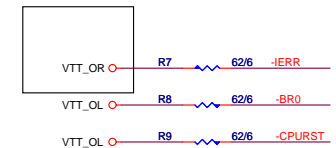
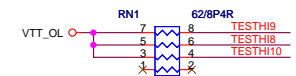
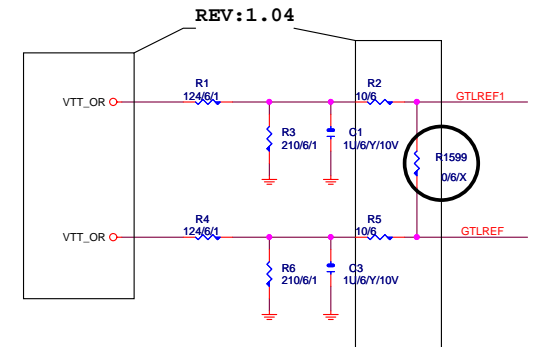
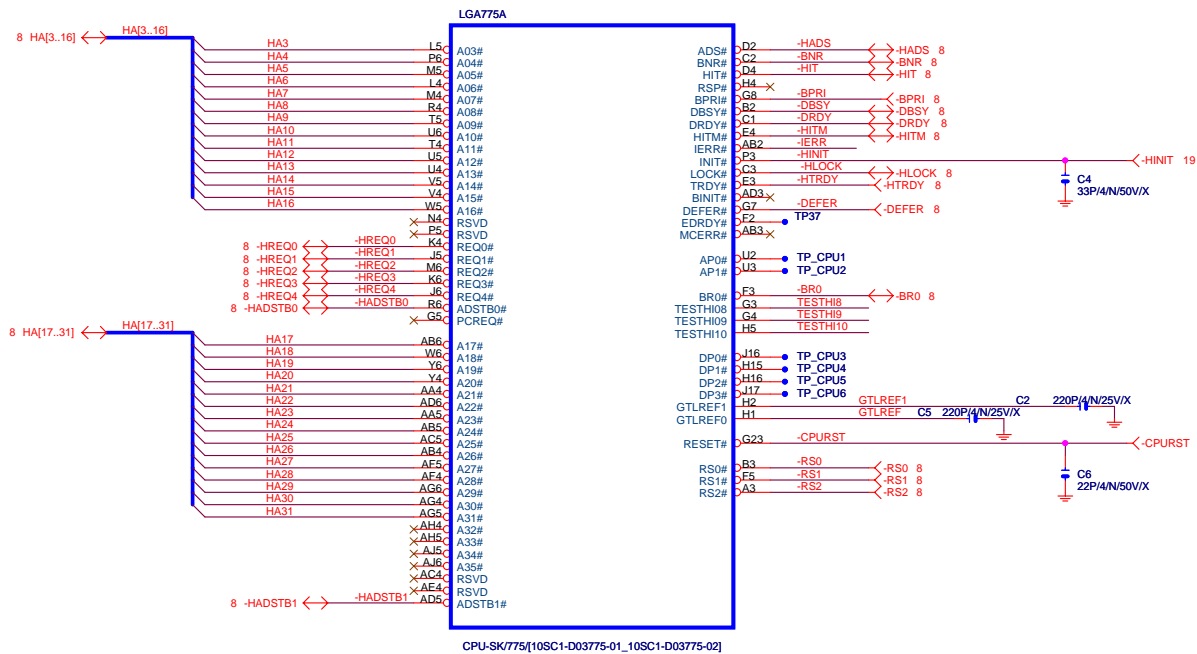
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Cover Sheet			
Size	Document Number	8I945G-PRO	Rev
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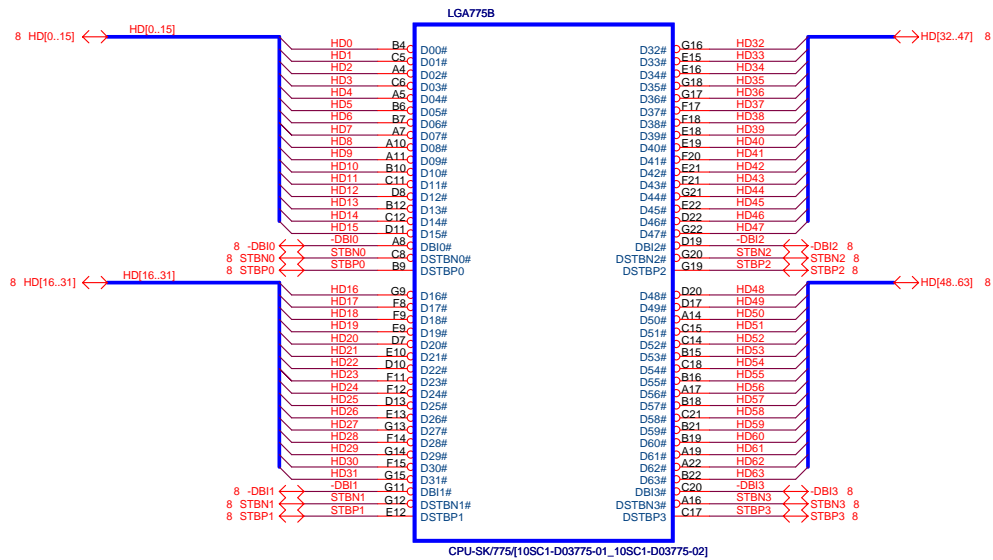
BLOCK DIAGRAM



**Version: 1.0**

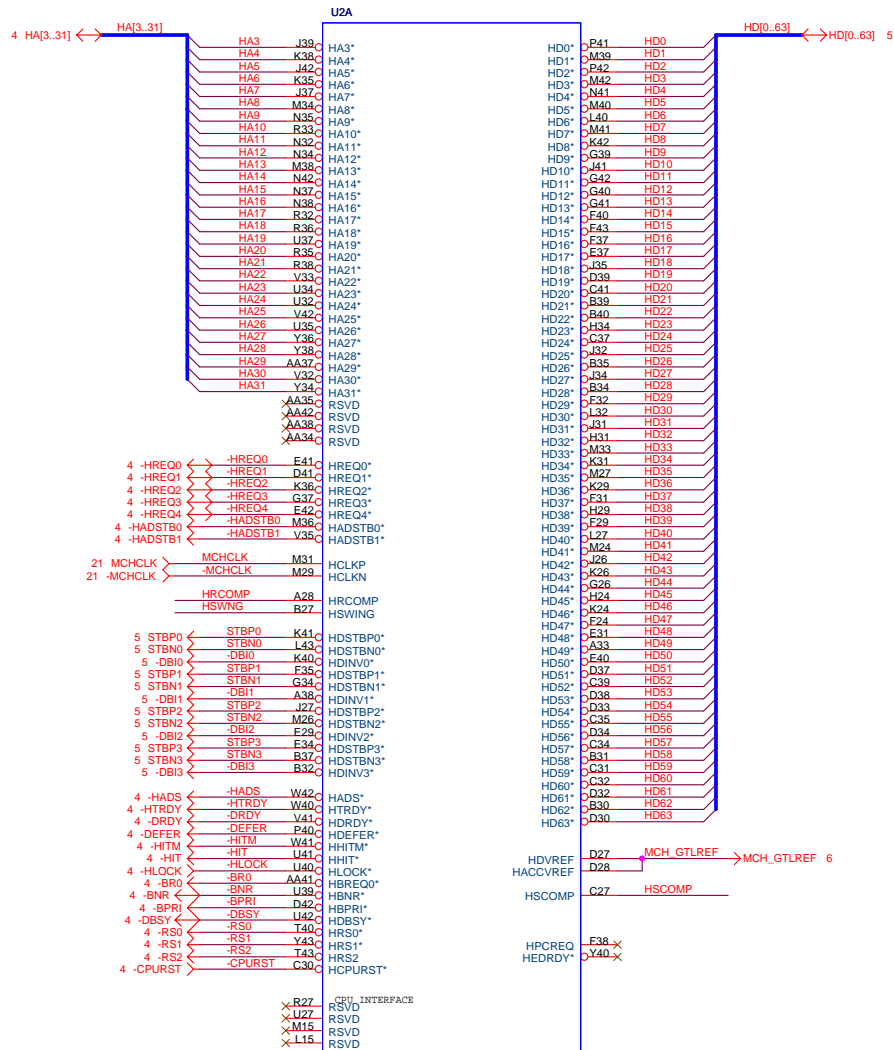
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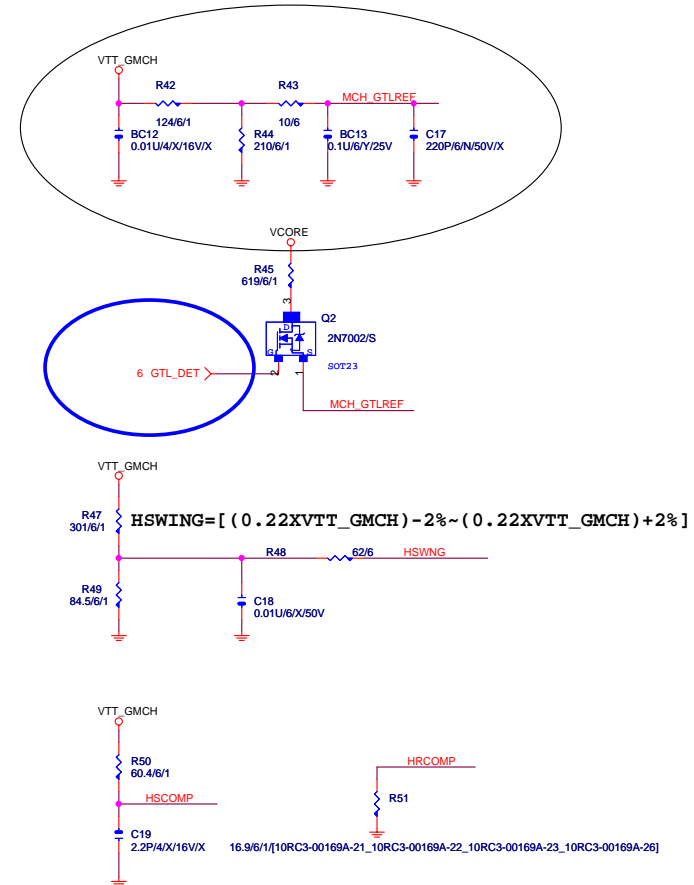




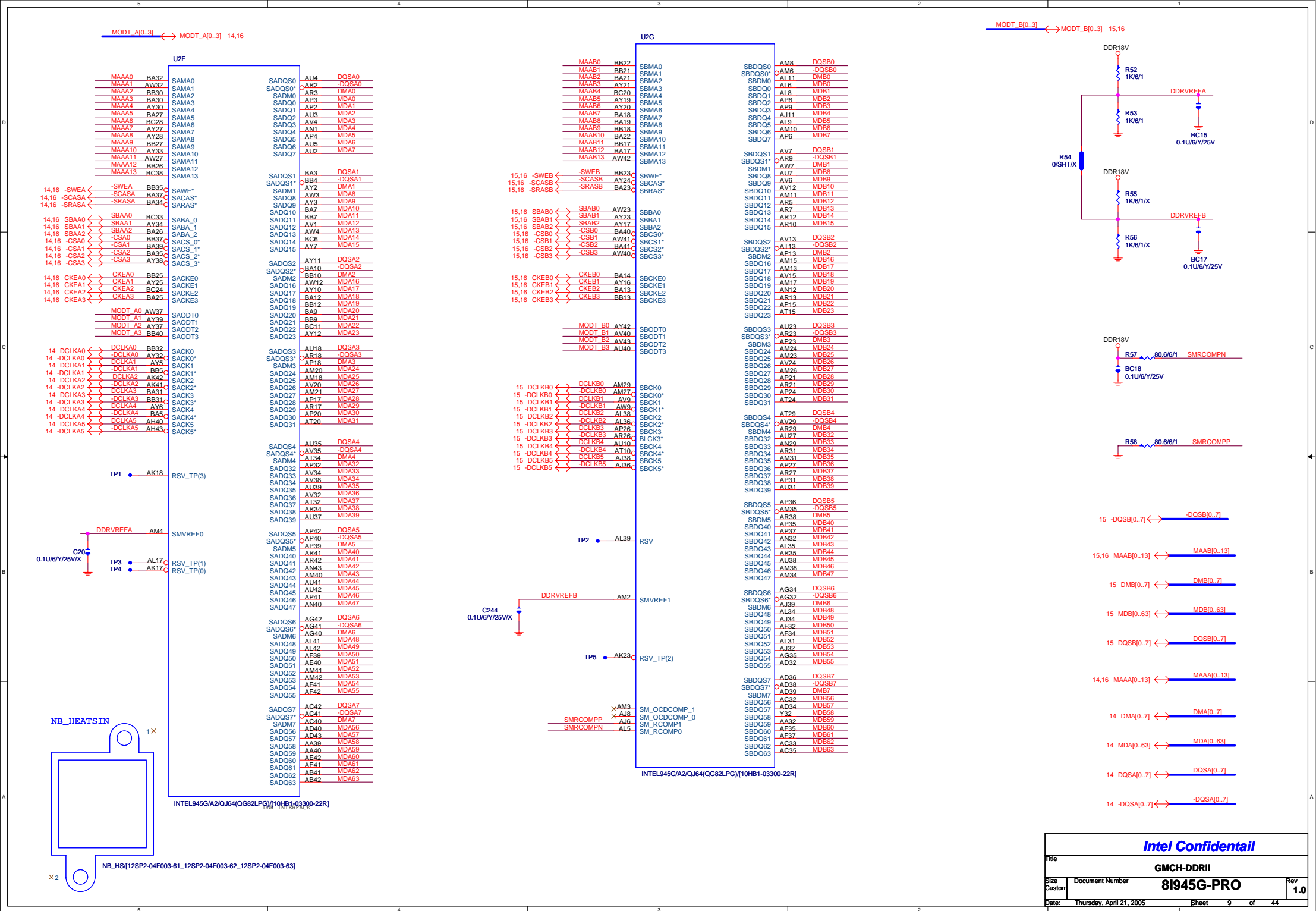




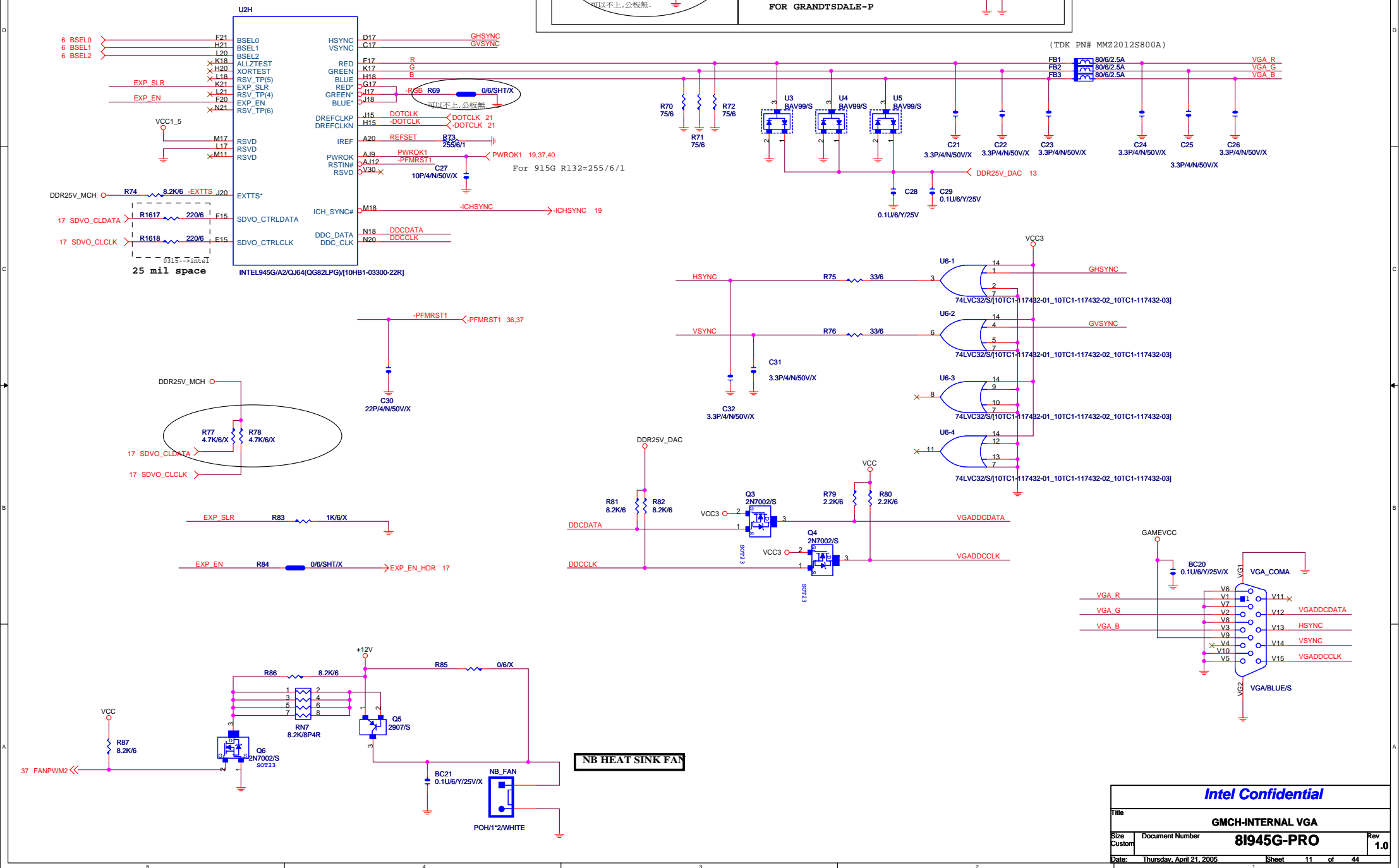
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U2C

A4	VSS	N2
A16	VSS	N6
A22	VSS	N8
A26	VSS	N13
A31	VSS	N15
A35	VSS	N24
B4	VSS	N26
B6	VSS	N27
B9	VSS	N29
B11	VSS	N31
B13	VSS	N33
B21	VSS	N36
B22	VSS	N39
B28	VSS	N43
B33	VSS	P3
B38	VSS	P14
C3	VSS	P15
C6	VSS	P24
C7	VSS	P26
C14	VSS	P27
C22	VSS	P29
C40	VSS	P30
D2	VSS	R6
D5	VSS	R9
D10	VSS	R12
D16	VSS	R14
D21	VSS	R30
D21	VSS	R31
D21	VSS	R34
E3	VSS	R37
E4	VSS	R39
E7	VSS	T2
E9	VSS	T42
E12	VSS	U3
E13	VSS	U5
E17	VSS	U9
E18	VSS	U12
E20	VSS	U14
E21	VSS	U31
E32	VSS	U33
F2	VSS	U36
F6	VSS	U38
F13	VSS	V2
F18	VSS	V8
F26	VSS	V11
F34	VSS	V12
F42	VSS	V14
G3	VSS	V34
G5	VSS	V36
G7	VSS	V37
G9	VSS	V38
G10	VSS	V39
G13	VSS	V43
G15	VSS	W3
G18	VSS	Y2
G20	VSS	Y5
G21	VSS	Y6
G24	VSS	Y9
G27	VSS	Y12
G29	VSS	Y14
G31	VSS	Y31
G32	VSS	Y36
G35	VSS	Y37
G38	VSS	Y39
H12	VSS	Y42
H17	VSS	AA3
H26	VSS	AA6
H27	VSS	AA11
J2	VSS	AA12
J5	VSS	AA21
J7	VSS	AA23
J10	VSS	AA31
J12	VSS	AA33
J21	VSS	AB2
J24	VSS	AB43
J29	VSS	AC2
J38	VSS	AC3
J43	VSS	AC7
K3	VSS	AC10
K5	VSS	AC14
K6	VSS	AC21
K7	VSS	AC23
K10	VSS	AC31
K12	VSS	AC36
K13	VSS	AC37
K15	VSS	AC38
K20	VSS	AC39
K27	VSS	
K32	VSS	
K34	VSS	
K37	VSS	
K39	VSS	
L2	VSS	
L12	VSS	
L13	VSS	
L24	VSS	
L26	VSS	
L29	VSS	
L31	VSS	
L42	VSS	
M3	VSS	
M5	VSS	
M6	VSS	
M9	VSS	
M10	VSS	
M13	VSS	
M20	VSS	
M21	VSS	
M55	VSS	
M37	VSS	

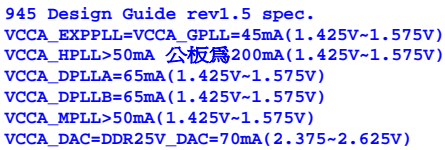
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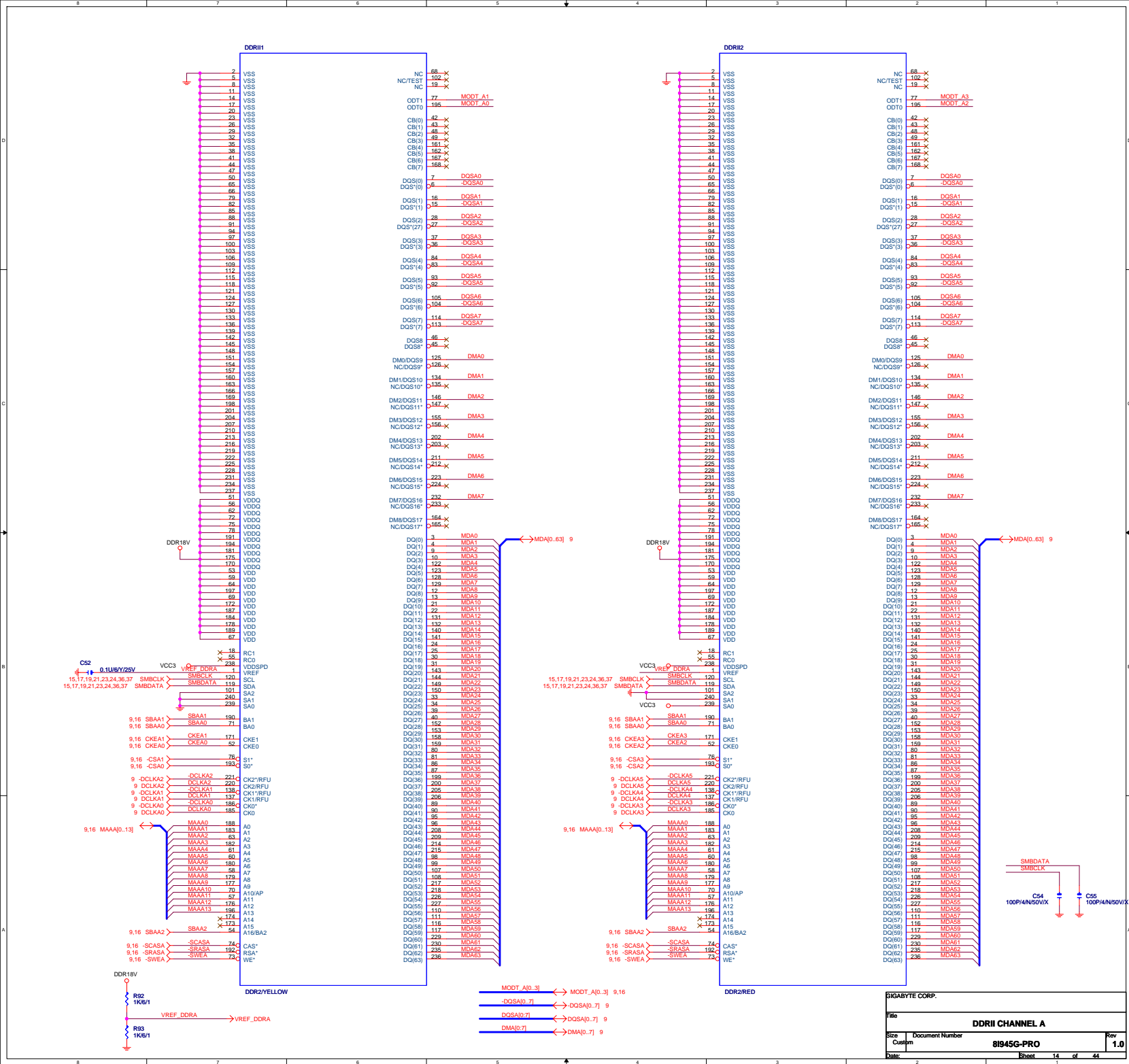
U2E

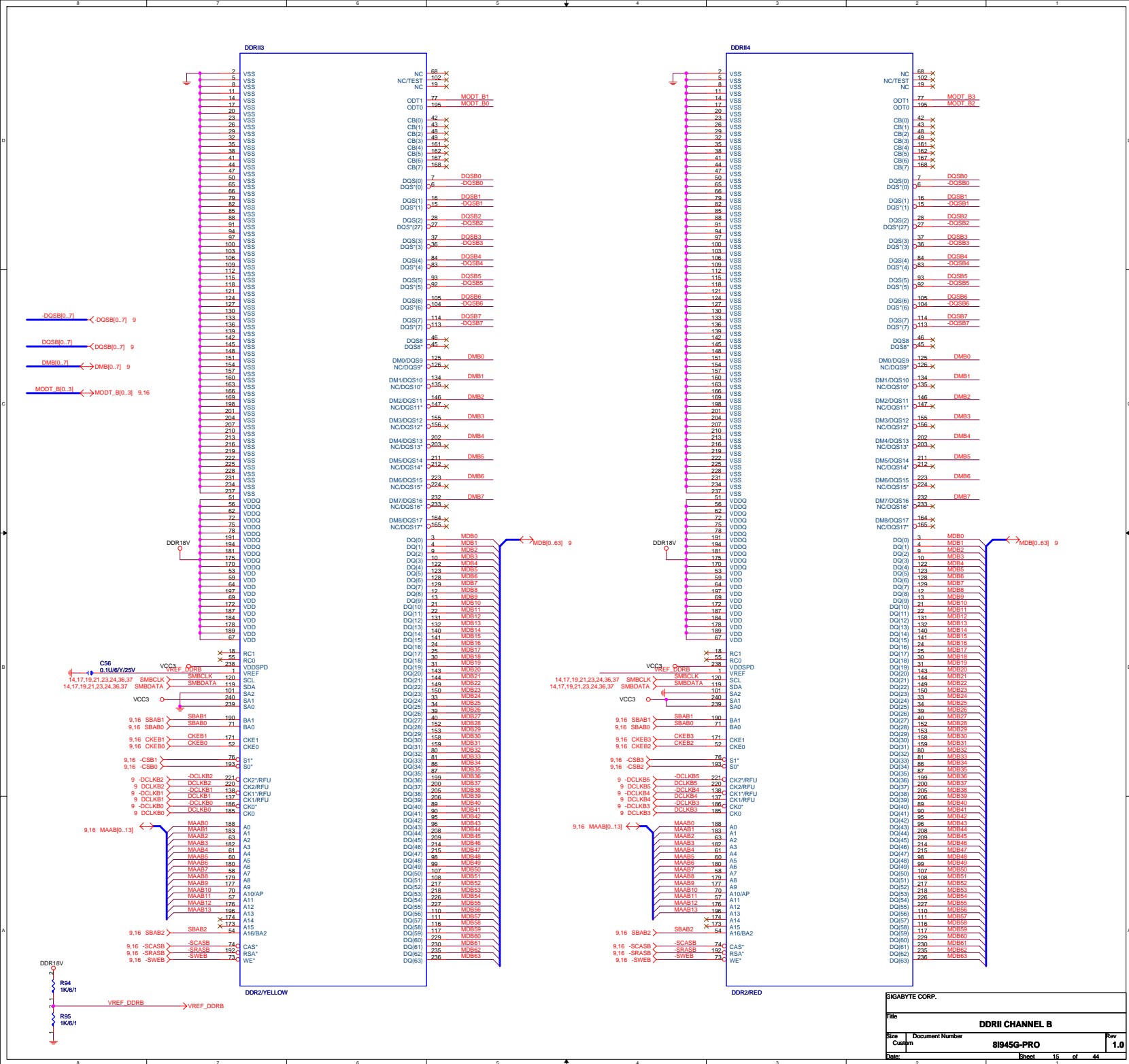
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AD9	VSS	AU26
AD11	VSS	AU29
AD13	VSS	AU32
AD33	VSS	AU34
AD35	VSS	AV2
AD37	VSS	AV10
AD42	VSS	AV17
AF1	VSS	AV37
AF2	VSS	AW10
AF3	VSS	BA4
AF5	VSS	BA42
AF33	VSS	BB3
AF36	VSS	BB6
AF38	VSS	BB11
AF43	VSS	BB14
AG30	VSS	BB19
AG31	VSS	BB34
AG33	VSS	BB39
AG36	VSS	BB41
AG37	VSS	BC9
AG38	VSS	A40
AG39	VSS	D1
AH42	VSS	D43
AJ7	VSS	R26
AJ10	VSS	R29
AJ30	VSS	U29
AJ31	VSS	V24
AJ33	VSS	V26
AJ35	VSS	V29
AJ37	VSS	W21
AK24	VSS	W23
AK26	VSS	W25
AK29	VSS	Y20
AK30	VSS	Y22
AL1	VSS	Y24
AL2	VSS	Y26
AL3	VSS	Y29
AL7	VSS	AA25
AL10	VSS	AA27
AL12	VSS	AA29
AL13	VSS	AC19
AL15	VSS	AC25
AL18	VSS	AC29
AL21	VSS	
AL23	VSS	
AL24	VSS	
AL27	VSS	
AL32	VSS	
AL33	VSS	
AL37	VSS	
AL43	VSS	
AM5	VSS	
AM7	VSS	
AM9	VSS	
AM33	VSS	
AM36	VSS	
AM37	VSS	
AM39	VSS	
AN2	VSS	
AN4	VSS	
AN13	VSS	
AN15	VSS	
AN17	VSS	
AN18	VSS	
AN20	VSS	
AN21	VSS	
AN23	VSS	
AN24	VSS	
AN26	VSS	
AN27	VSS	
AN31	VSS	
AN42	VSS	
AP5	VSS	
AP7	VSS	
AP10	VSS	
AP12	VSS	
AP29	VSS	
AP34	VSS	
AP38	VSS	
AR1	VSS	
AR6	VSS	
AR15	VSS	
AR20	VSS	
AR24	VSS	
AR32	VSS	
AR37	VSS	
AR39	VSS	
AR43	VSS	
AT12	VSS	
AT17	VSS	
AT18	VSS	
AT21	VSS	
AT23	VSS	
AT26	VSS	
AT27	VSS	
AT31	VSS	
AU6	VSS	
AU8	VSS	
AU12	VSS	
AU13	VSS	
AU15	VSS	
AU17	VSS	
AU20	VSS	
AD16	VSS	
AD20	VSS	
AD22	VSS	
AD24	VSS	
AD27	VSS	
AD29	VSS	
AE19	VSS	
AE21	VSS	
AE23	VSS	
AE25	VSS	
AF18	VSS	
AF20	VSS	
AF22	VSS	
AF24	VSS	
AV1	VSS	
BC4	VSS	

INTEL945G/A2/QJ64(QG82LPG)[10HB1-03300-22R]

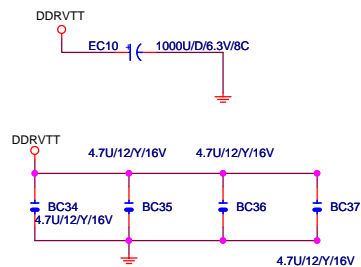
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Custom	Document Number	81945G-PRO	Rev 1.0
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Sheet 12 of 44			

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1.0

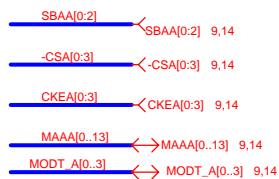
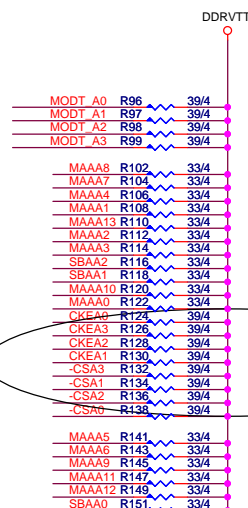
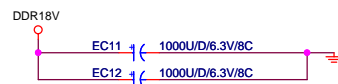




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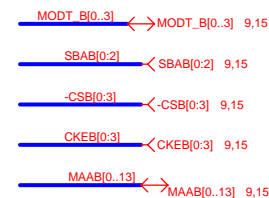
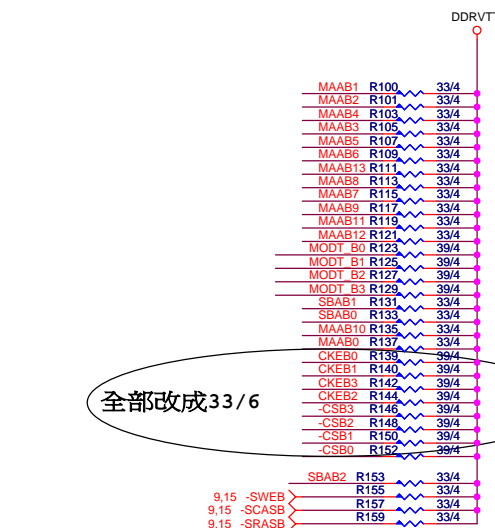
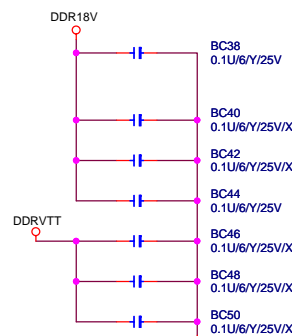


## DDR TERMINATION CHANNEL A



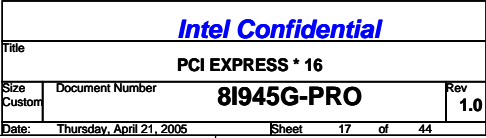
## DDRVTT Decouple

## CHANNEL B



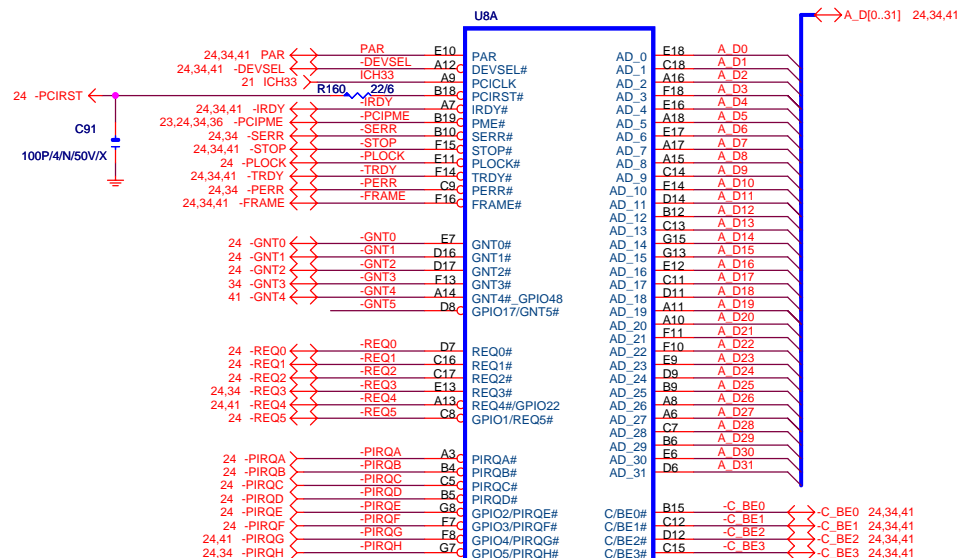
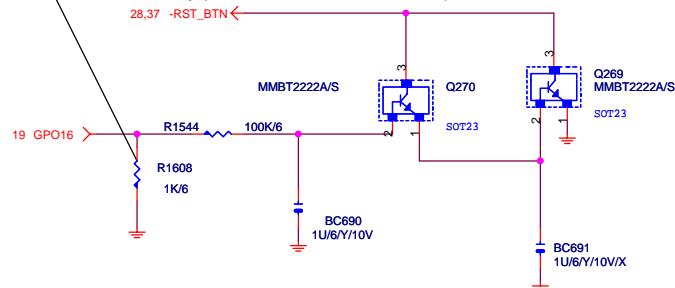
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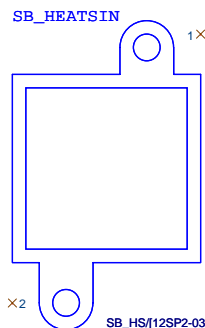


FOR ICH7R POWER ON 瞬間會HIGH 到1.8V 之後0V,必須PULL DOWN 1K/6

H/W  
RESET

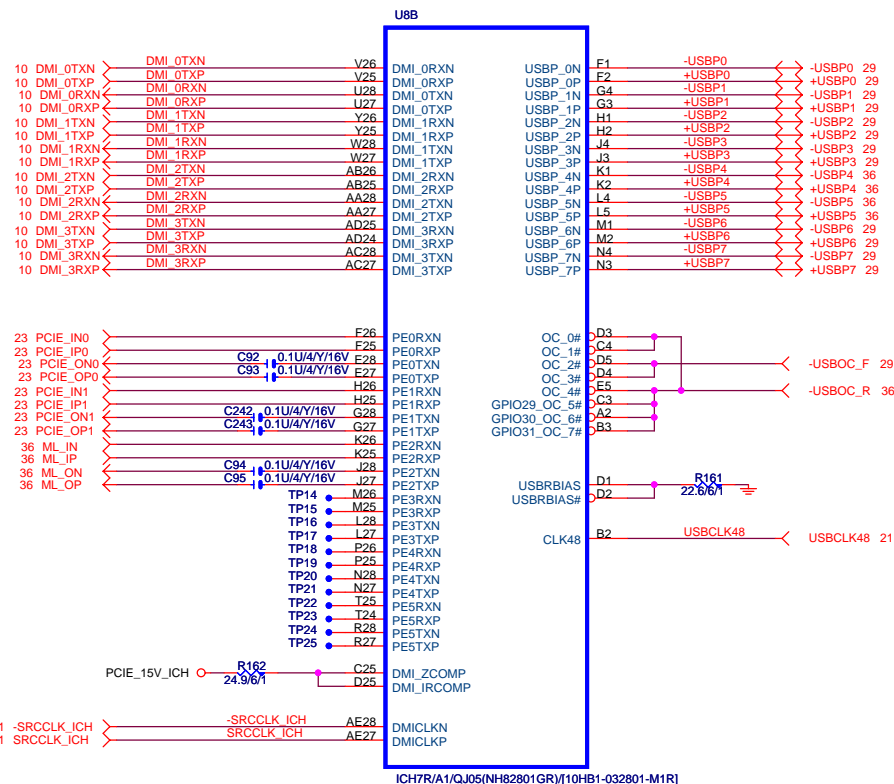
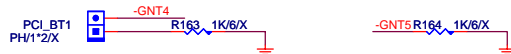


ICH7R/A1/QJ05(NH82801GR)[10HB1-032801-M1R]  
01=SPI  
10=PCI  
11=LPC(DEFAULT)  
10HB1-032801-M1 REV:NON



PCI\_BT2: OPEN FWH

CLOSE SPI



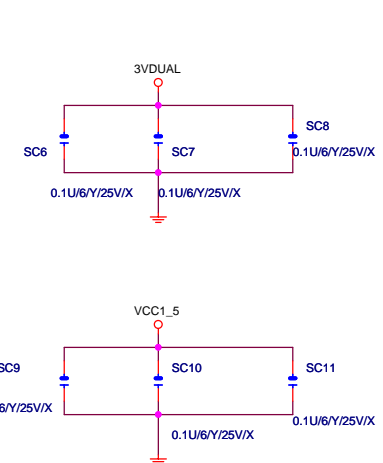
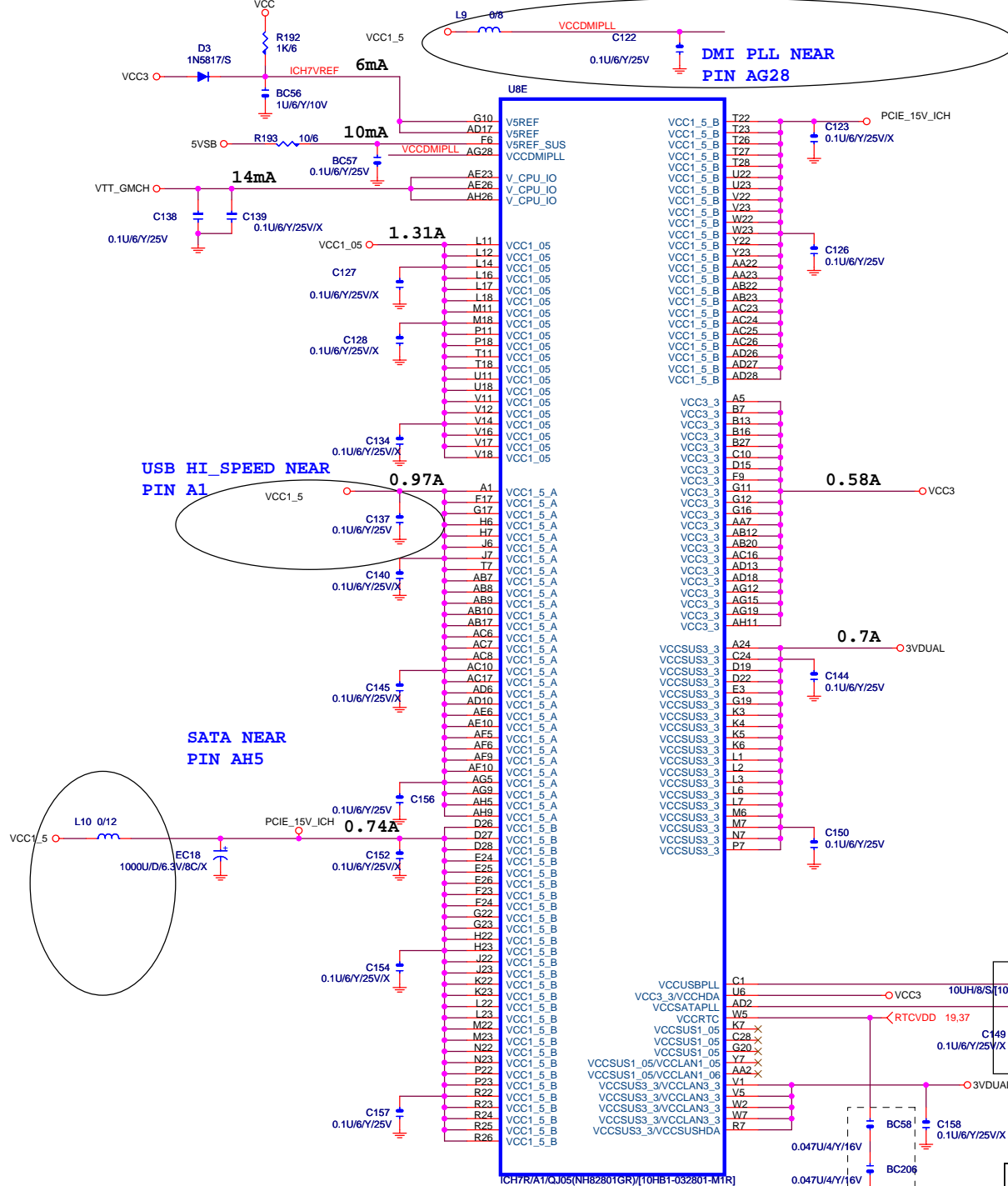
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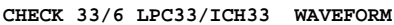
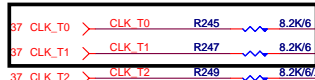


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A23	VSS2	VSS102	R15
B1	VSS3	VSS103	R16
B8	VSS4	VSS104	R17
B11	VSS5	VSS105	R18
B14	VSS6	VSS106	T8
B17	VSS7	VSS107	T12
B20	VSS8	VSS108	T13
B26	VSS9	VSS109	T14
B28	VSS10	VSS110	T15
C2	VSS11	VSS111	T16
C6	VSS12	VSS112	T17
D10	VSS13	VSS113	U4
D13	VSS14	VSS114	U12
D18	VSS15	VSS115	U13
D21	VSS16	VSS116	U14
D24	VSS17	VSS117	U15
E1	VSS18	VSS118	U16
E2	VSS19	VSS119	U17
E8	VSS20	VSS120	U24
E15	VSS21	VSS121	U25
F3	VSS22	VSS122	U26
F4	VSS23	VSS123	V2
F5	VSS24	VSS124	V13
F12	VSS25	VSS125	V15
F27	VSS26	VSS126	V24
F28	VSS27	VSS127	V27
G1	VSS28	VSS128	V28
G2	VSS29	VSS129	W6
G5	VSS30	VSS130	W24
G6	VSS31	VSS131	W25
G9	VSS32	VSS132	W26
G14	VSS33	VSS133	Y3
G18	VSS34	VSS134	Y24
G21	VSS35	VSS135	Y27
G24	VSS36	VSS136	Y28
G25	VSS37	VSS137	AA1
G26	VSS38	VSS138	AA24
H3	VSS39	VSS139	AA26
H4	VSS40	VSS140	AB4
H24	VSS41	VSS141	AB6
H27	VSS43	VSS143	AB11
H28	VSS44	VSS144	AB14
J1	VSS45	VSS145	AB16
J2	VSS46	VSS146	AB19
J5	VSS47	VSS147	AB21
J24	VSS48	VSS148	AB24
J25	VSS49	VSS149	AB27
J26	VSS50	VSS150	AB28
K24	VSS51	VSS151	AC2
K27	VSS52	VSS152	AC5
K28	VSS53	VSS153	AC9
L13	VSS54	VSS154	AC11
L15	VSS55	VSS155	AD1
L24	VSS56	VSS156	AD3
L25	VSS57	VSS157	AD4
L26	VSS58	VSS158	AD7
M3	VSS59	VSS159	AD8
M4	VSS60	VSS160	AD11
M5	VSS61	VSS161	AD15
M12	VSS62	VSS162	AD19
M13	VSS63	VSS163	AD23
M14	VSS64	VSS164	AE2
M15	VSS65	VSS165	AE4
M16	VSS66	VSS166	AE8
M17	VSS67	VSS167	AE11
M24	VSS68	VSS168	AE13
M27	VSS69	VSS169	AE18
M28	VSS70	VSS170	AE21
N1	VSS71	VSS171	AE24
N2	VSS72	VSS172	AE2
N5	VSS73	VSS173	AF4
N6	VSS74	VSS174	AF8
N11	VSS75	VSS175	AF11
N12	VSS76	VSS176	AF1
N13	VSS77	VSS177	AF27
N14	VSS78	VSS178	AF28
N15	VSS79	VSS179	AG1
N16	VSS80	VSS180	AG3
N17	VSS81	VSS181	AG7
N18	VSS82	VSS182	AG14
N24	VSS83	VSS183	AG17
N25	VSS84	VSS184	AG20
N26	VSS85	VSS185	AG26
P3	VSS86	VSS186	AH1
P4	VSS87	VSS187	AH3
P12	VSS88	VSS188	AH7
P13	VSS89	VSS189	AH12
P14	VSS90	VSS190	AH23
P15	VSS91	VSS191	AH27
P16	VSS92	VSS192	C27
P17	VSS93	VSS193	E4
P24	VSS94	VSS194	AG11
P27	VSS95		
P28	VSS96		
R1	VSS97		
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R13	VSS100		

ICH7R/A1/QJ05(NH82801GR)[10HB1-032801-M1R]

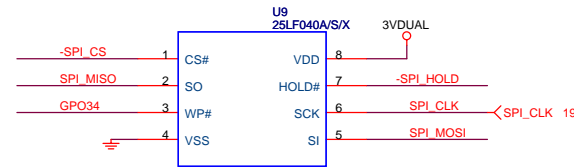
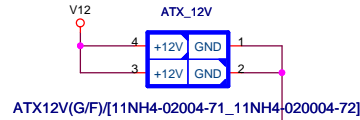
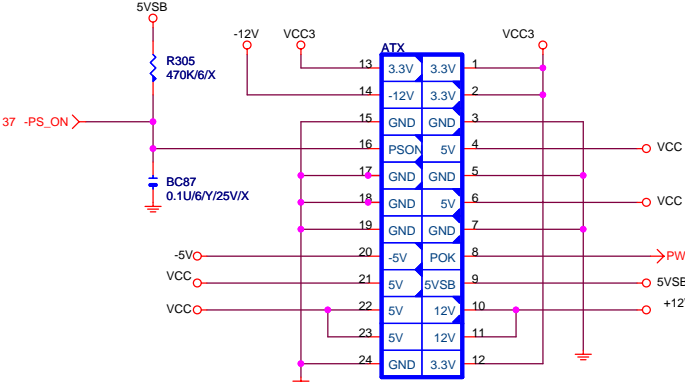


8.2K/6/X R194

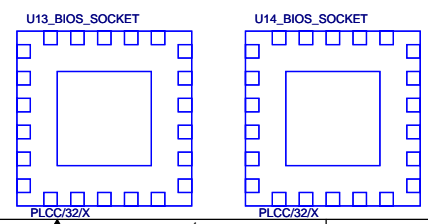
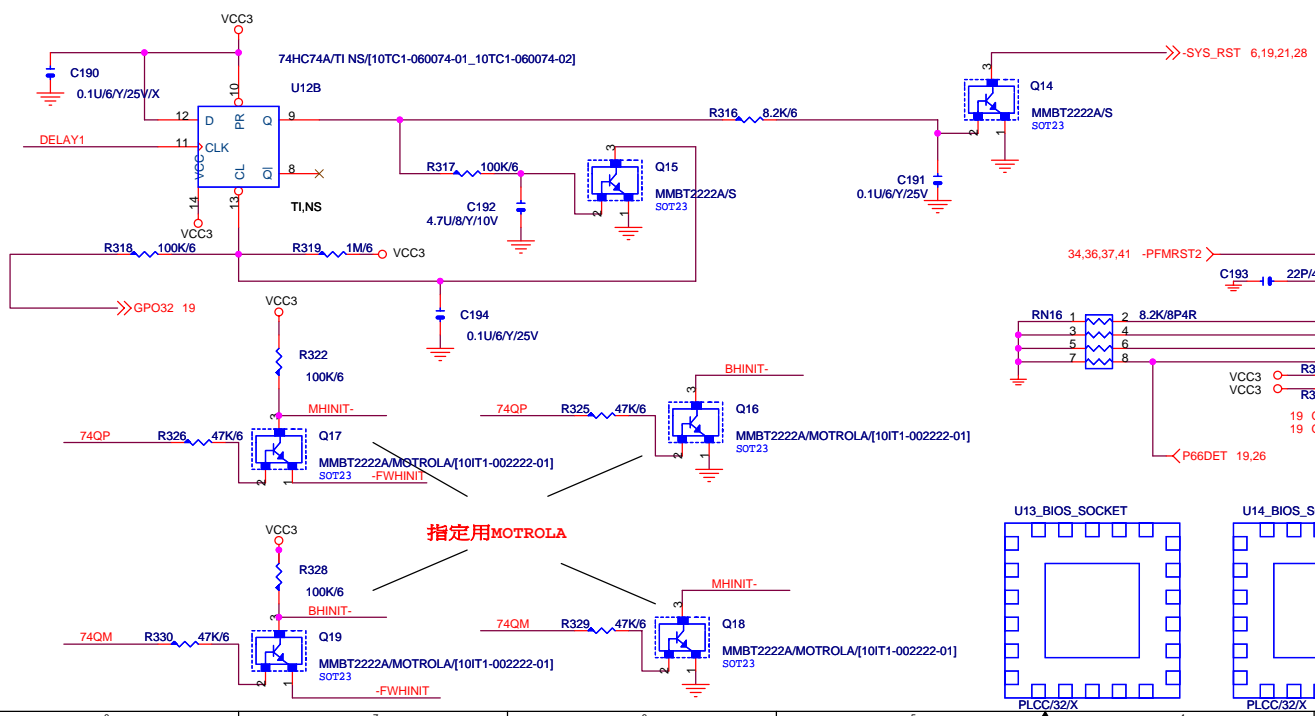
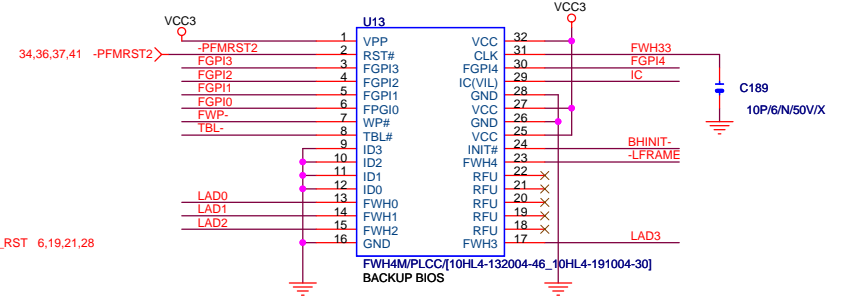
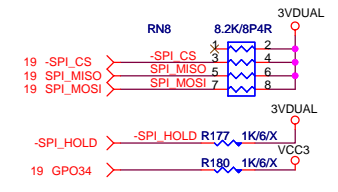
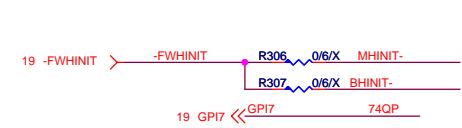
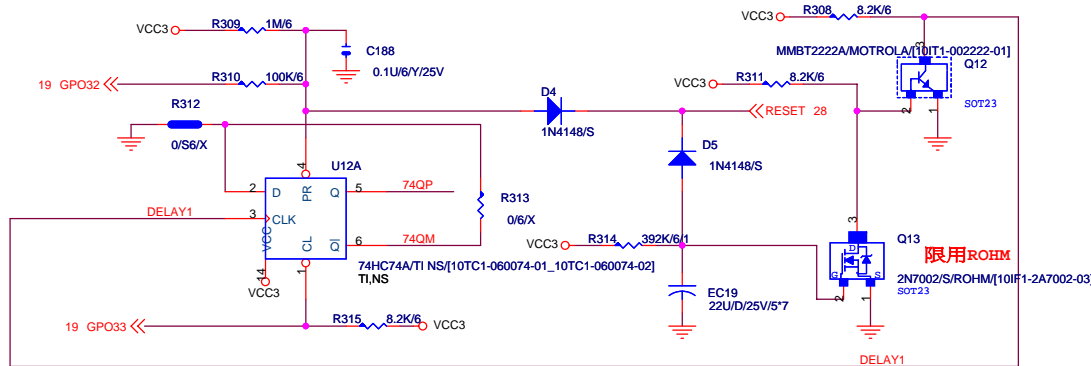


Title			
ICS954148AF			
Size	Document Number		Rev
Custom	81945G-PRO		1.0
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# ATX POWER CONNECTOR



ATX 24PIN(G/F)[11NH4-020024-31\_11NH4-020024-32]



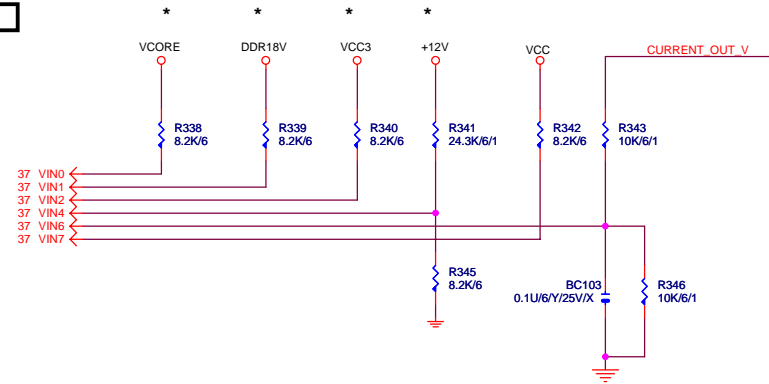




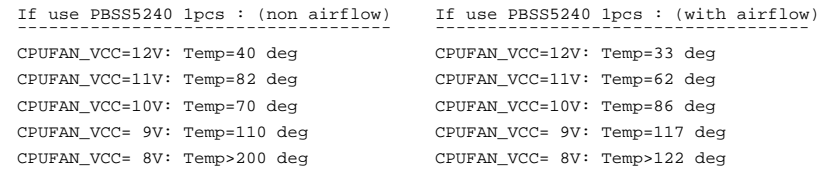




## VOLTAGE SENSE



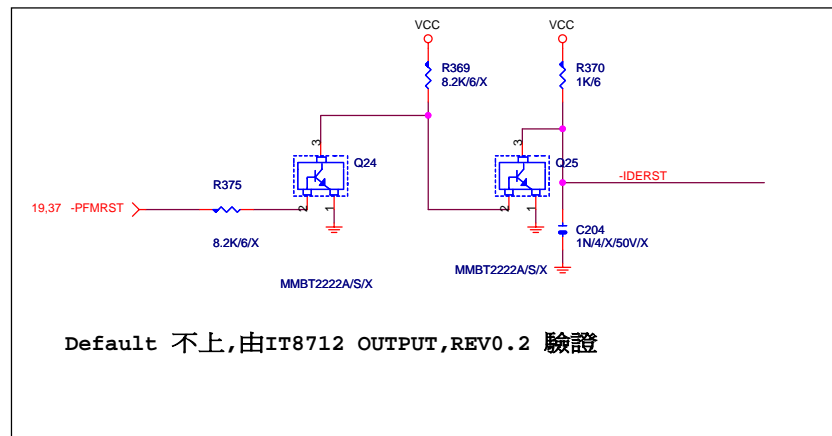
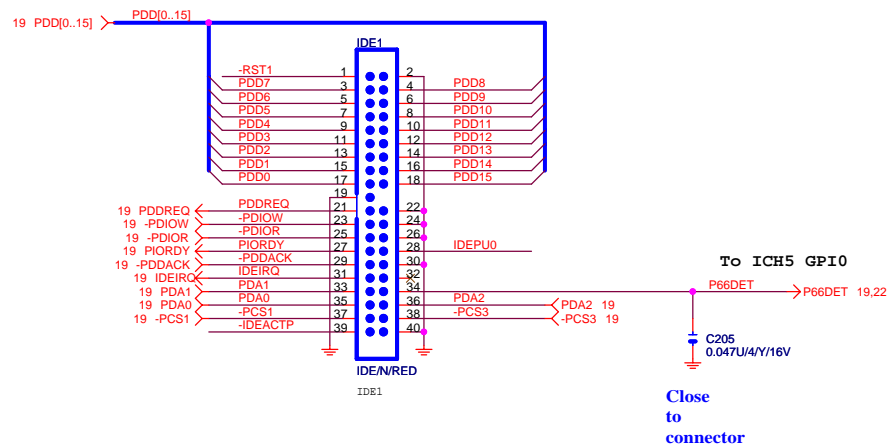
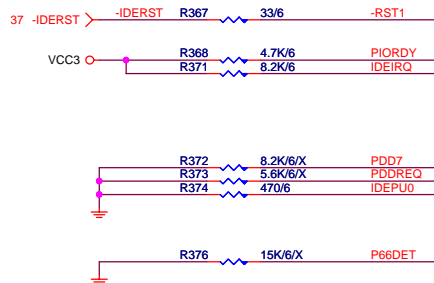
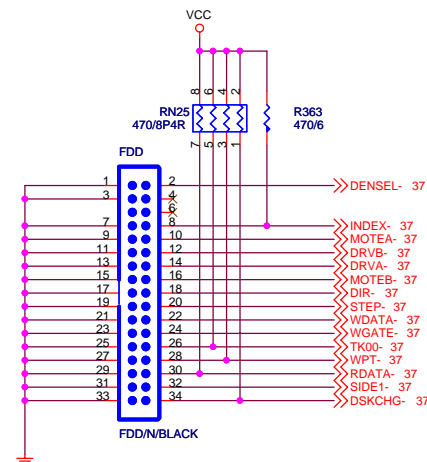
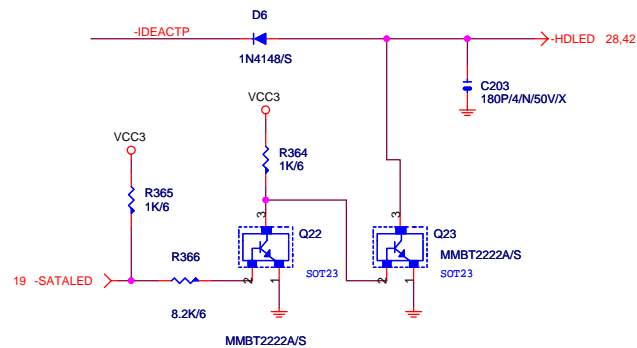
## CPU/SYS FAN

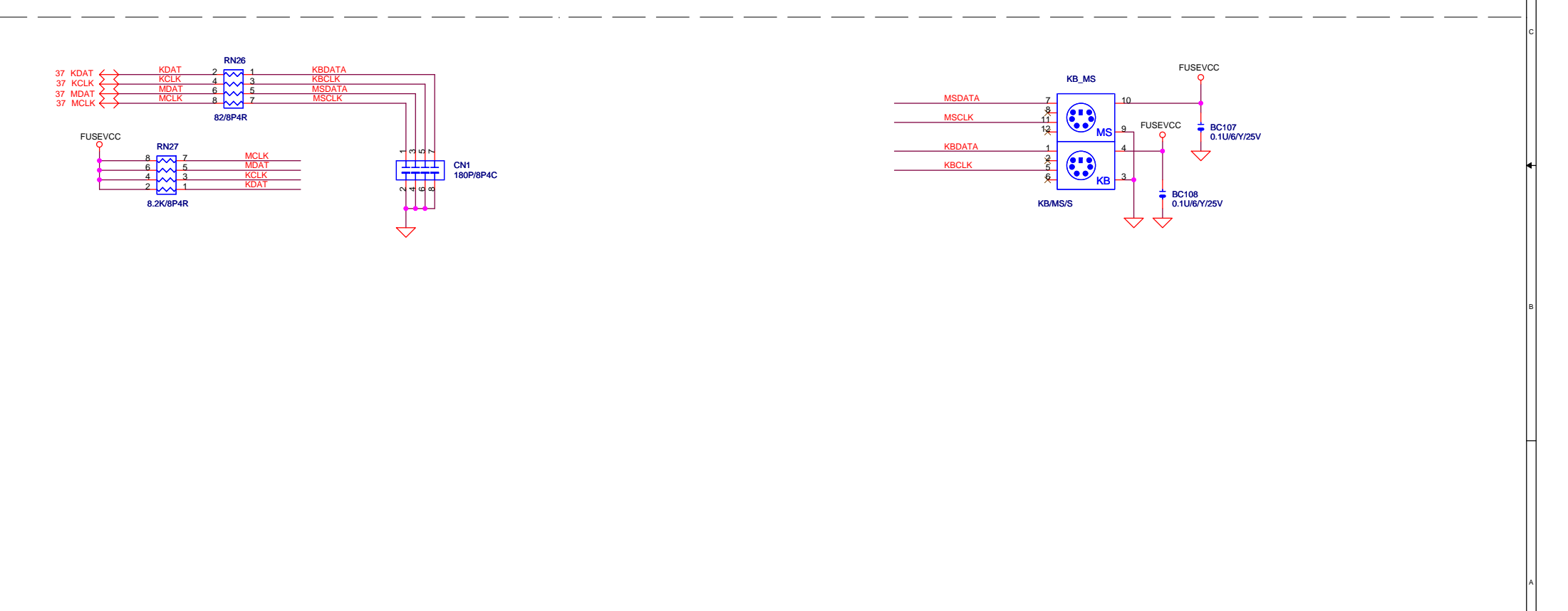
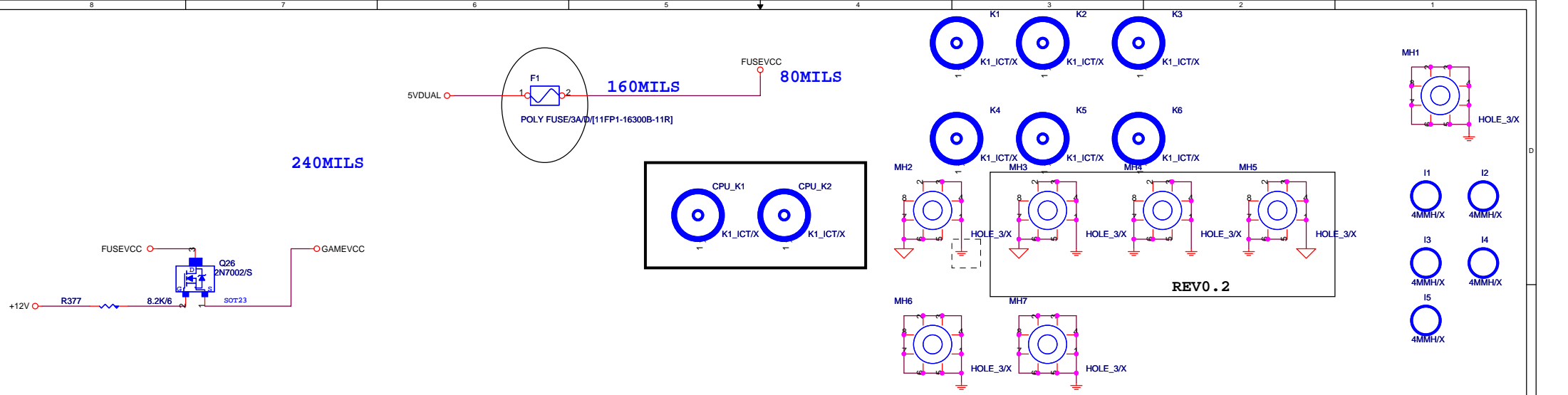


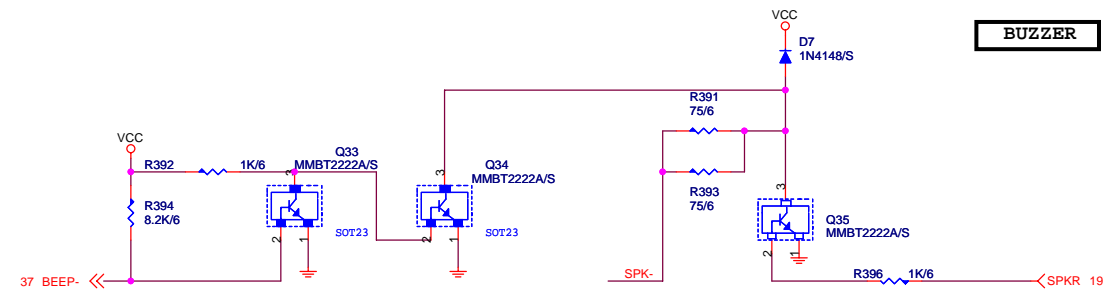
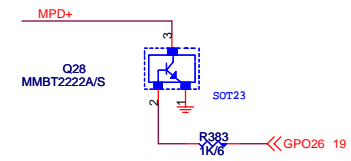
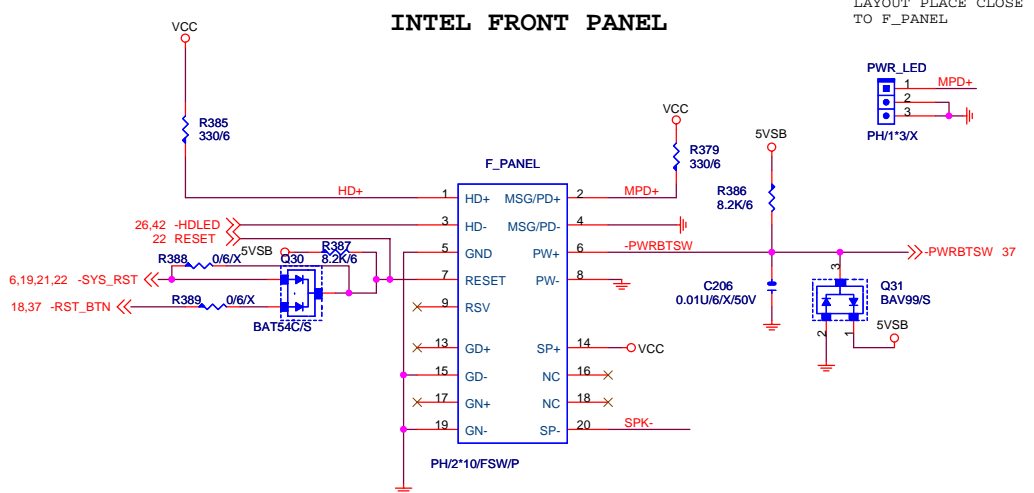
default is high  
gpiox pin 可以3.3v or 5v  
if 3pin fan 則開機後將program is low

**GIGABYTE**

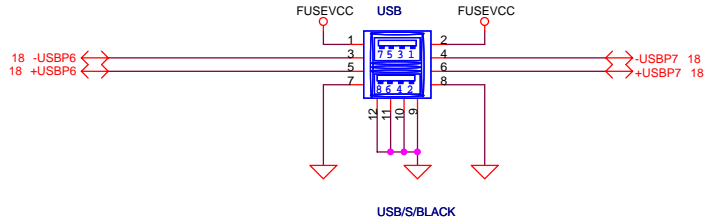
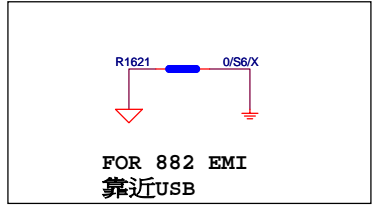
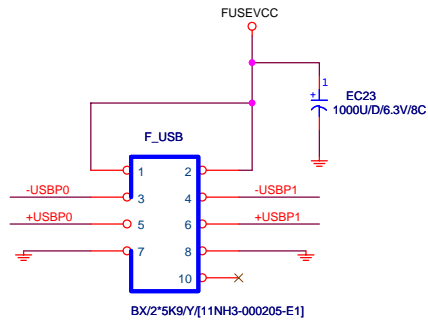
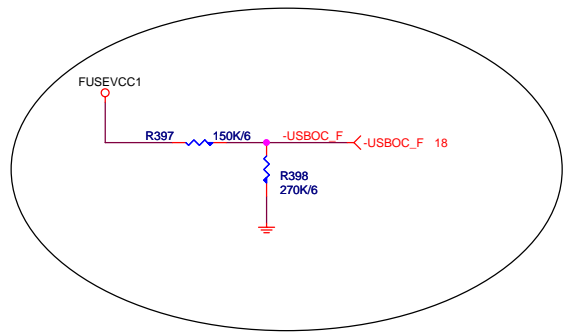
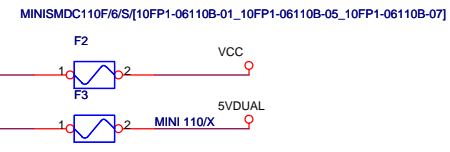
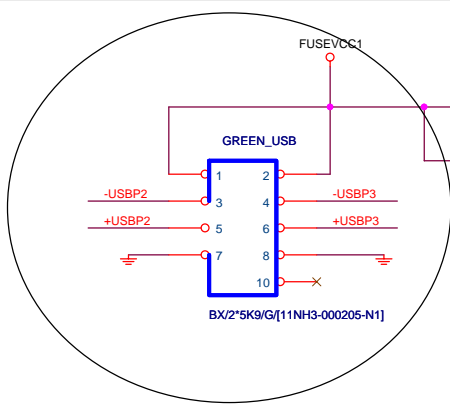
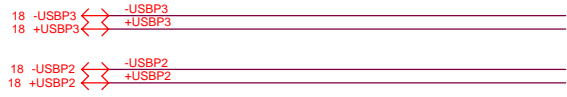
Title			
<b>HWM/FAN/CI/BIOS</b>			
Size	Document Number		Rev
Custom	<b>81945G-PRO</b>		<b>1.0</b>
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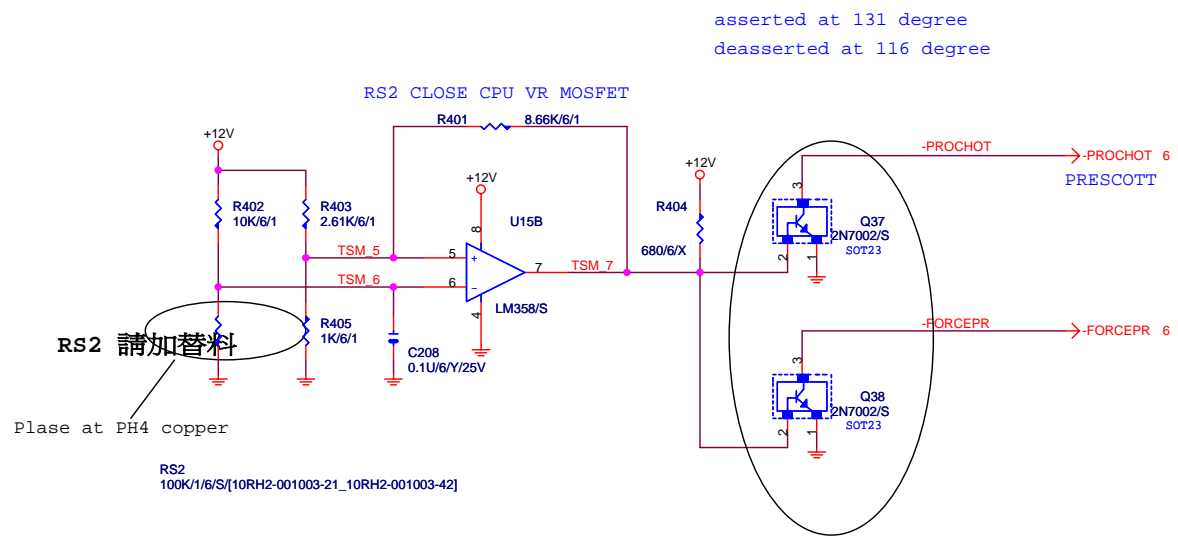




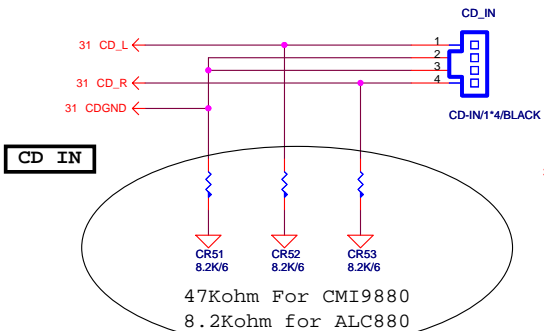
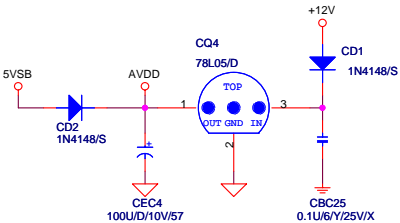
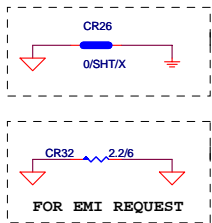


# FRONT USB





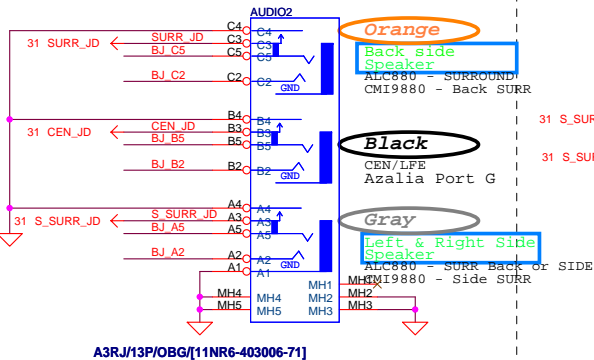
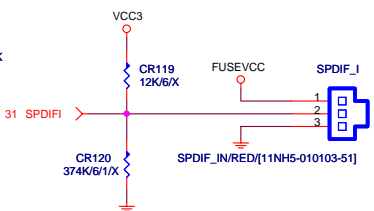
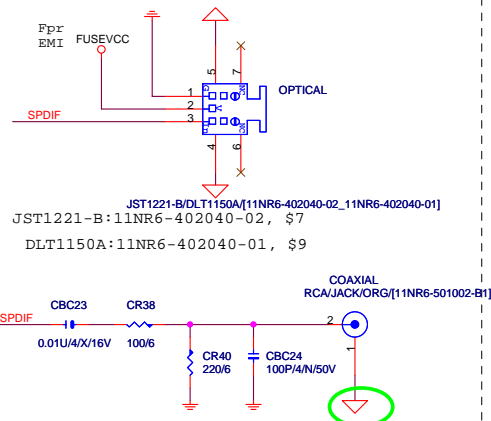
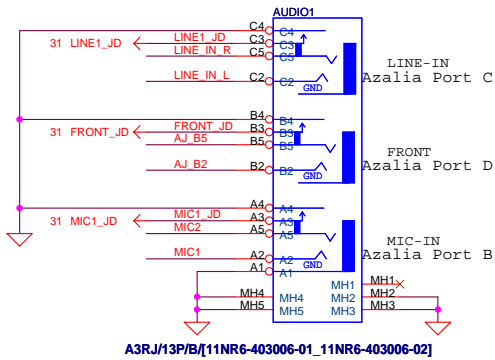




Azalia Jack

Normal --> pin4/pin3 open

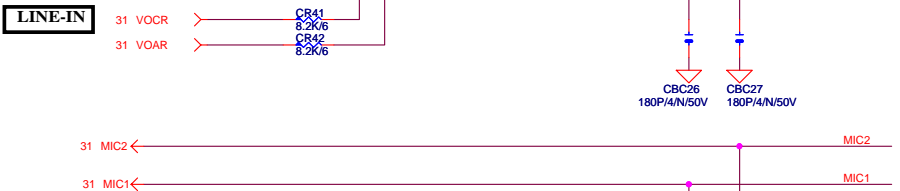
Plug jack --> pin4/pin3 close



## LINE OUT FRONT OUT



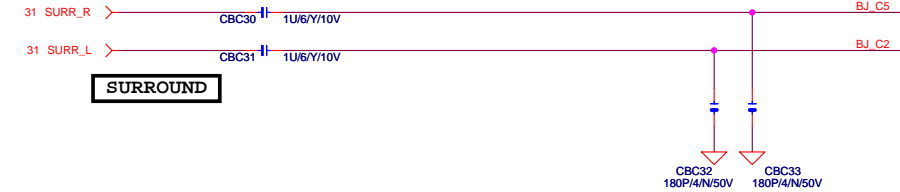
## LINE-IN



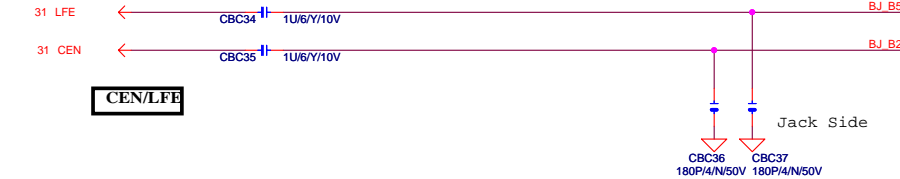
## MIC



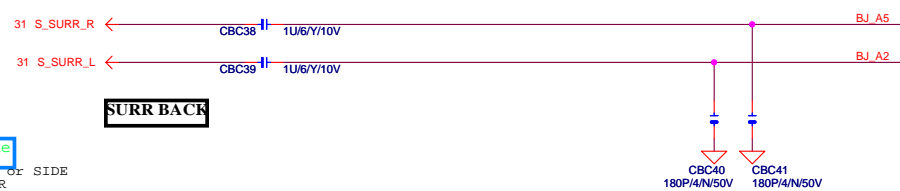
## SURROUND



## CEN/LFE



## SURR BACK

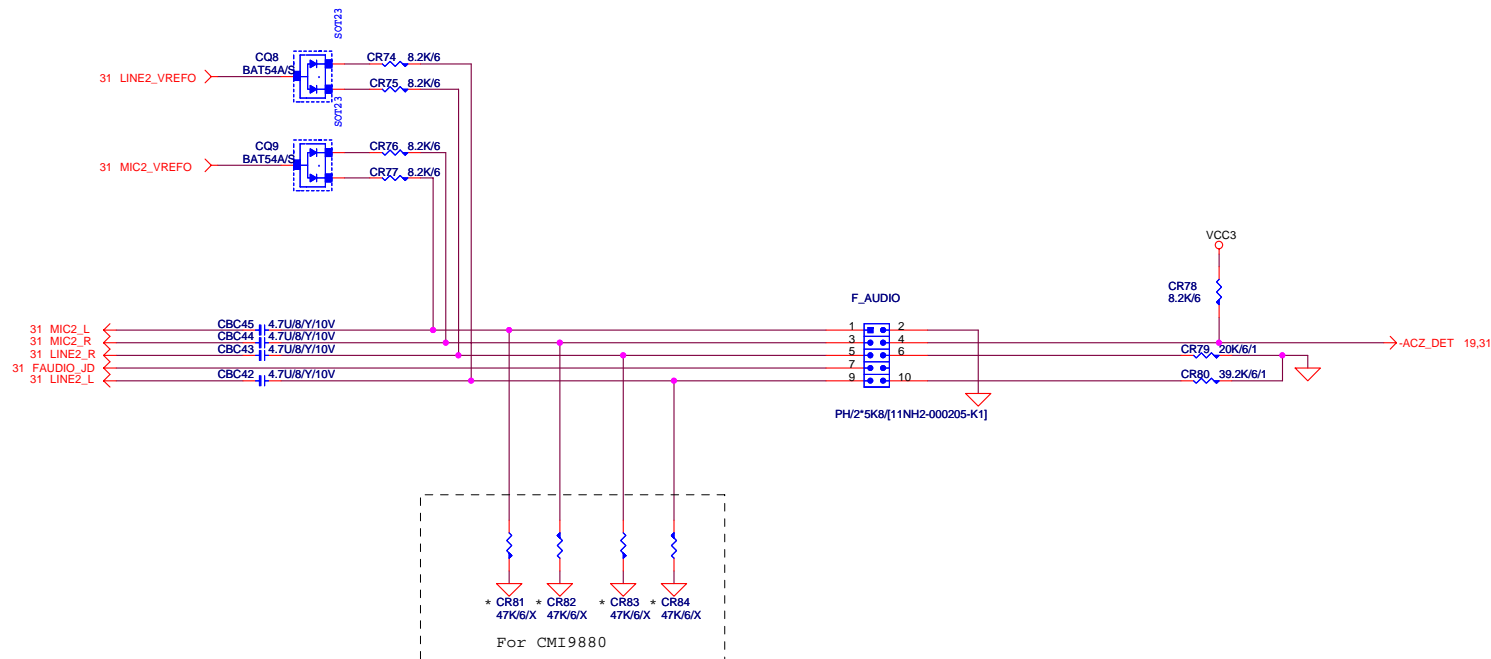


Intel Confidential			
Title			
AUDIO JACK			
81945G-PRO			
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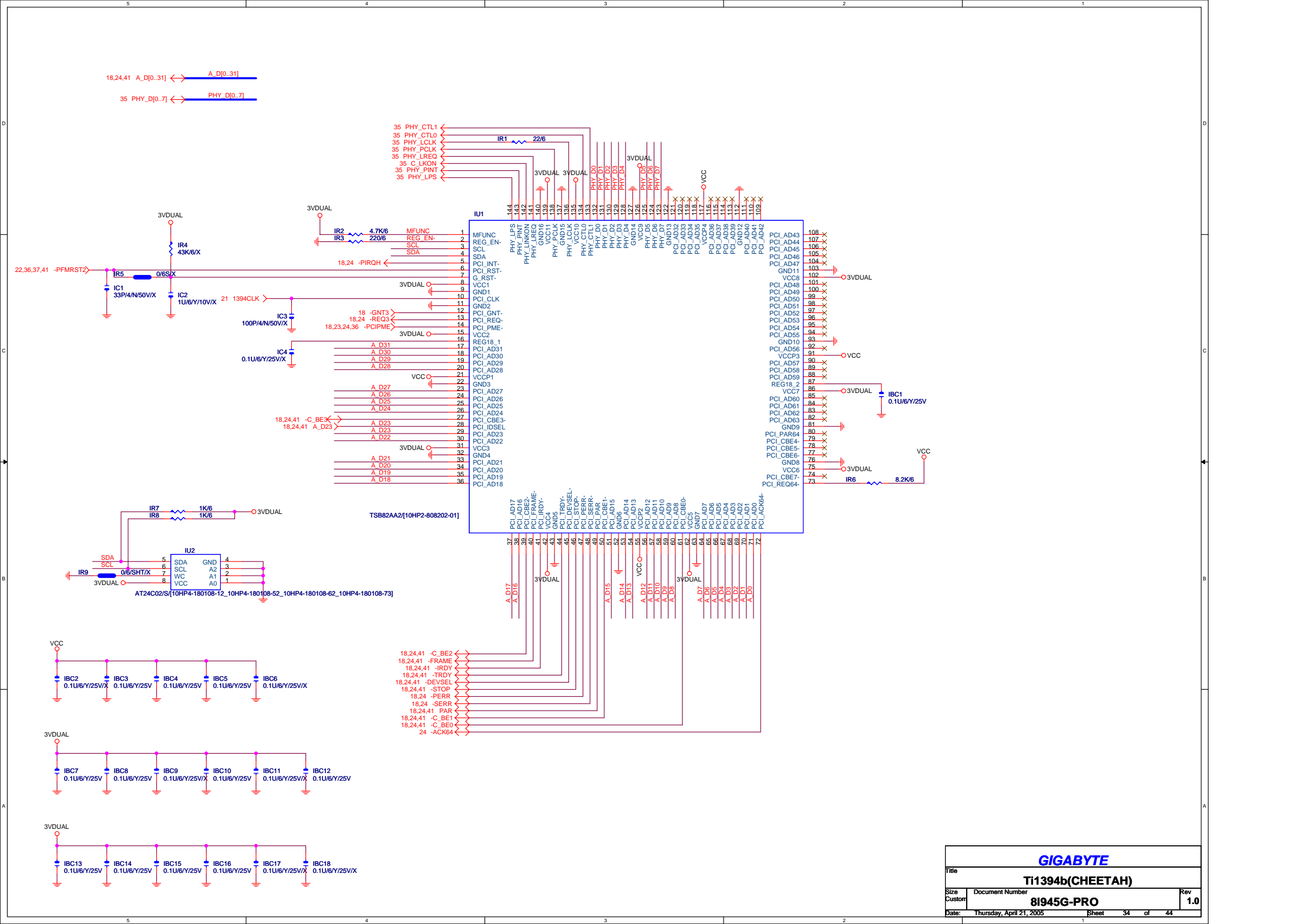
Azalia Port F

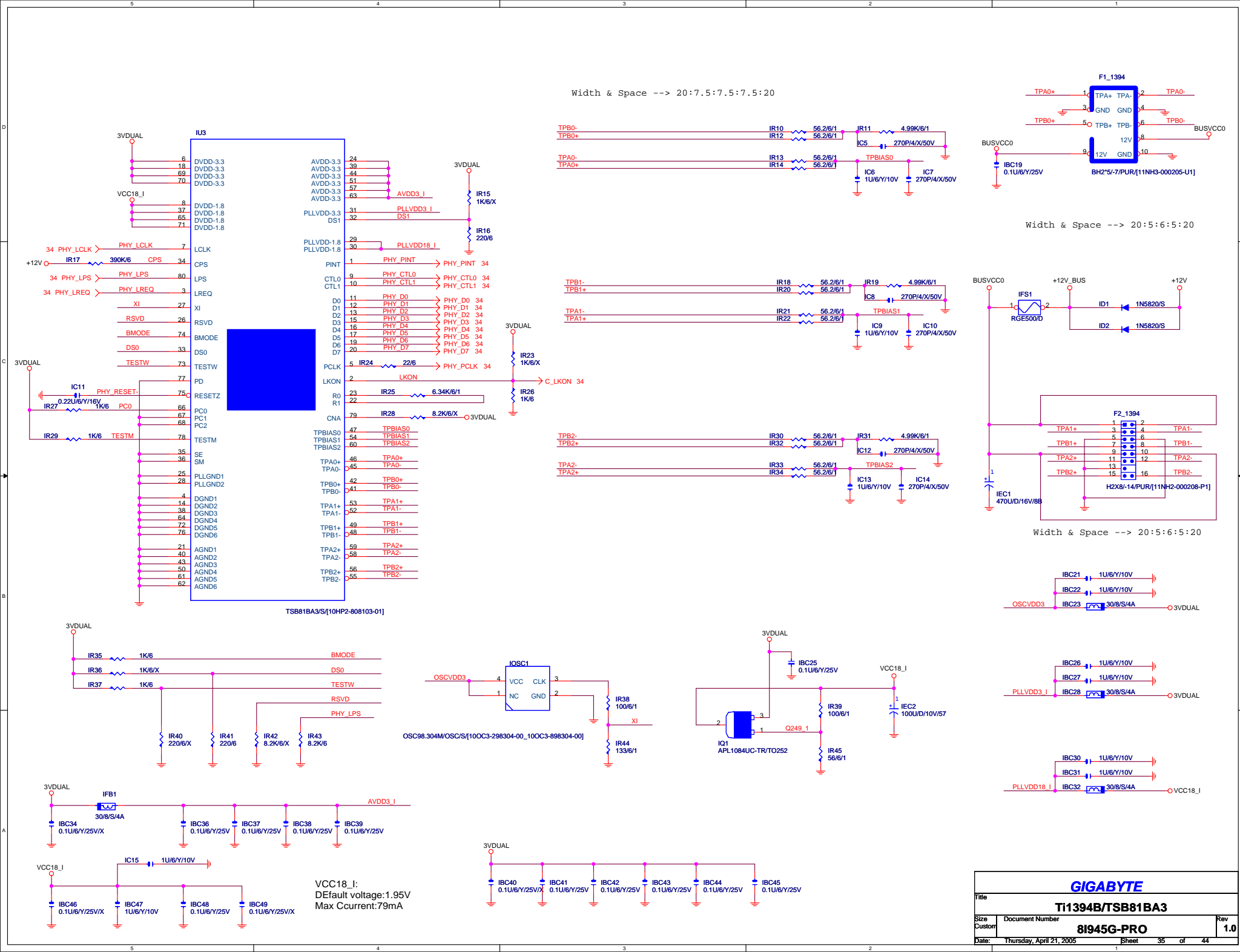
Azalia Port E

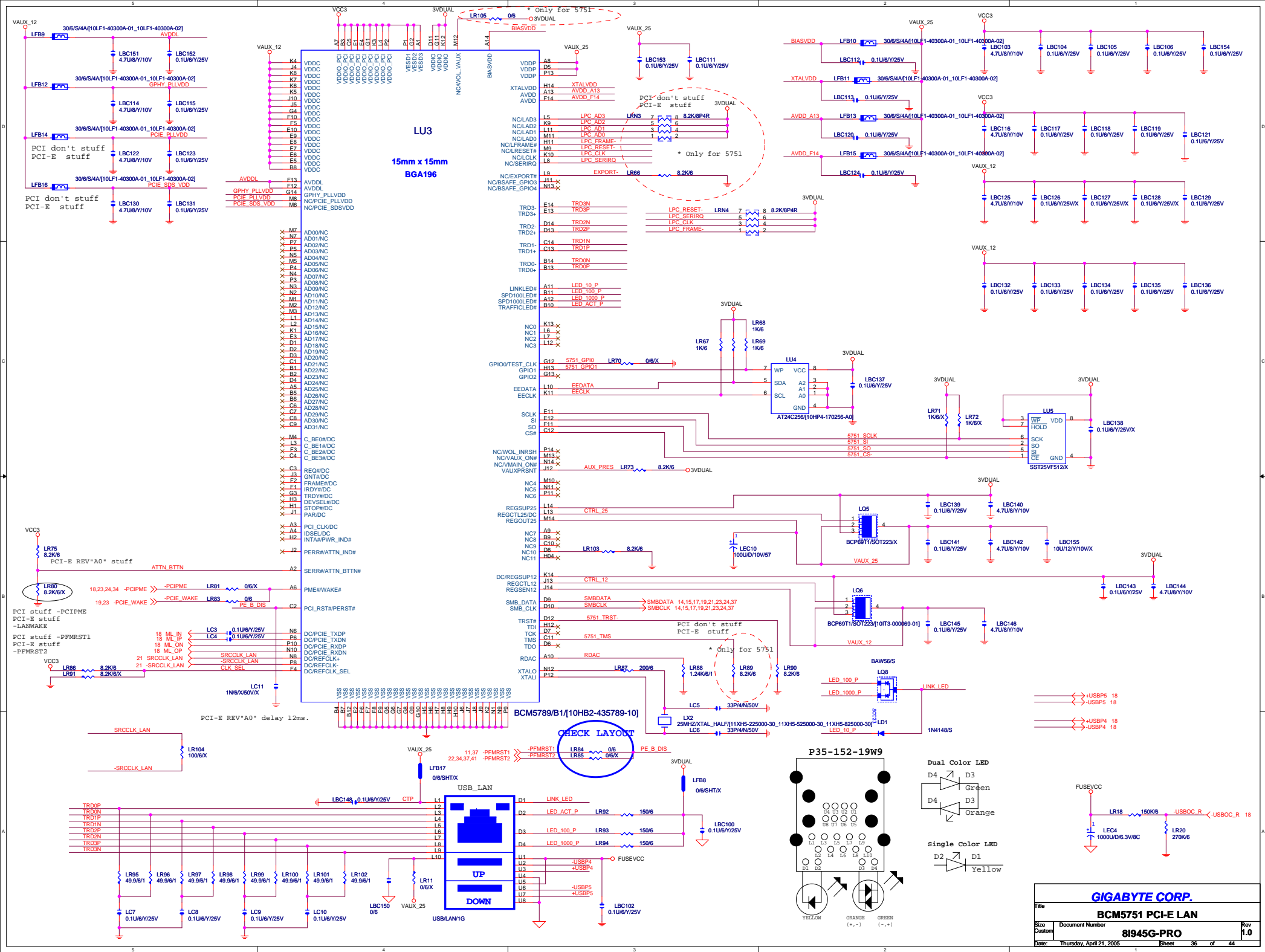


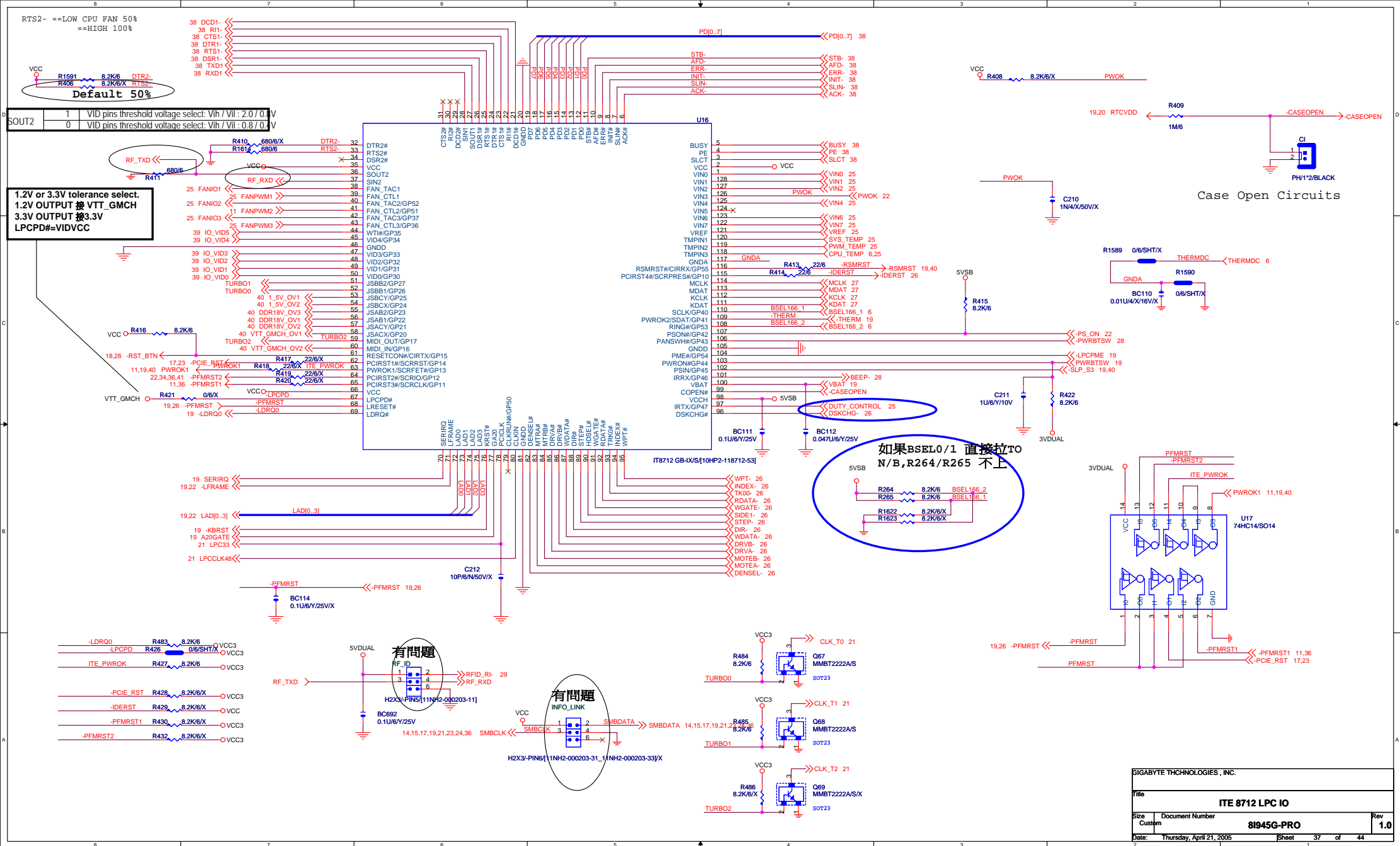
Intel Confidential

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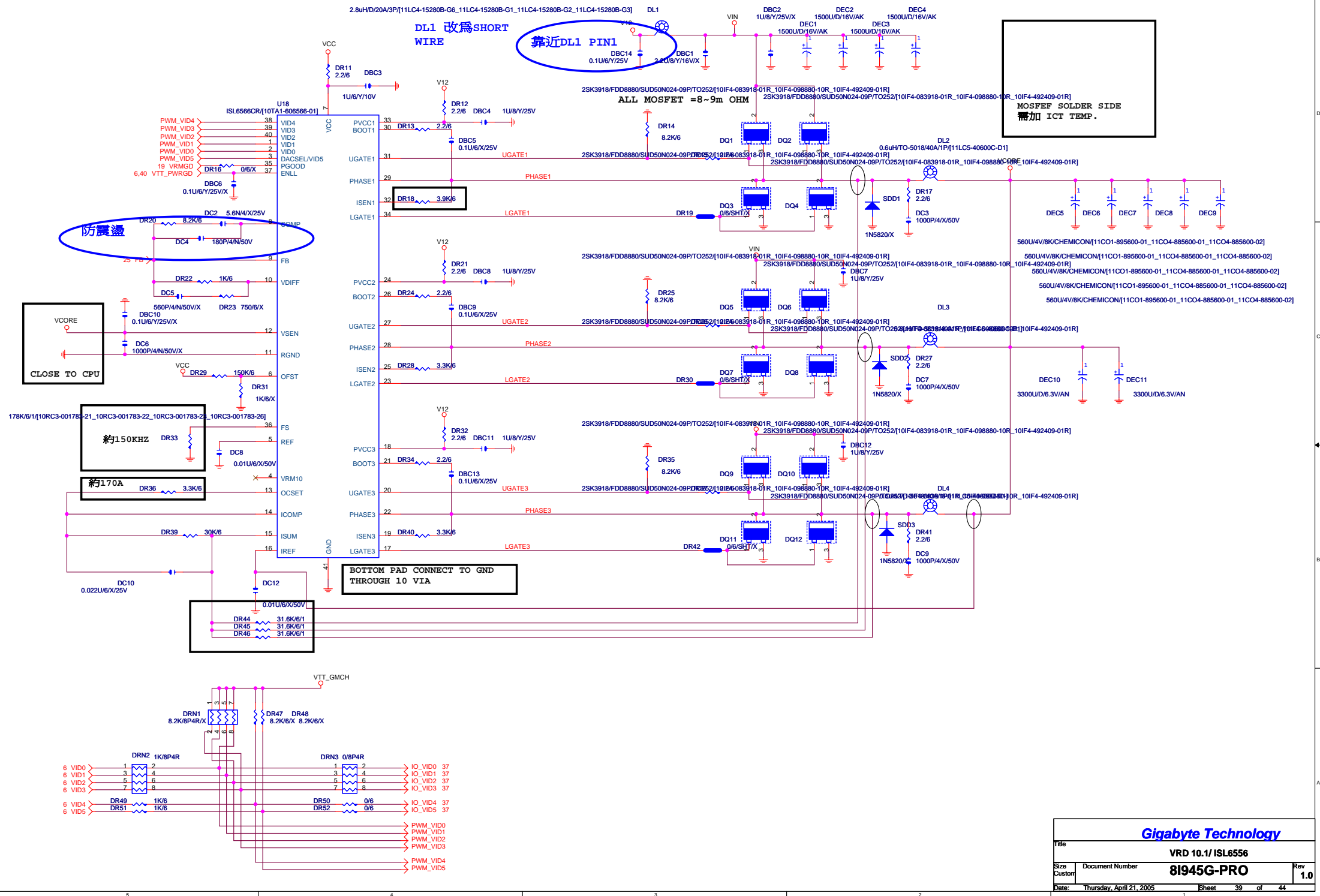


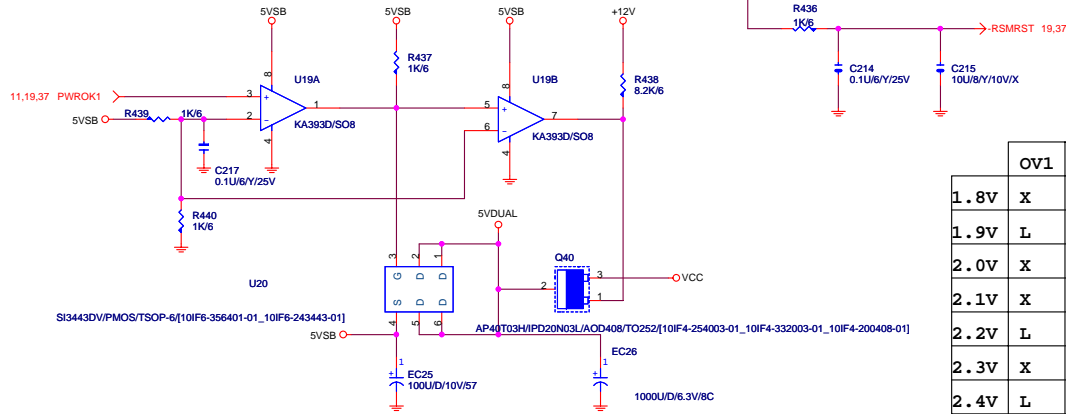
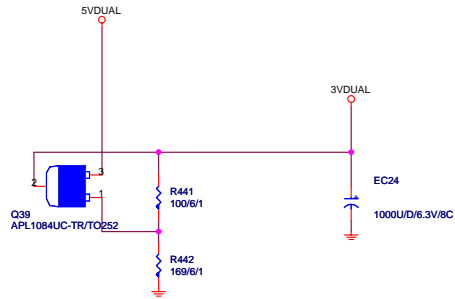




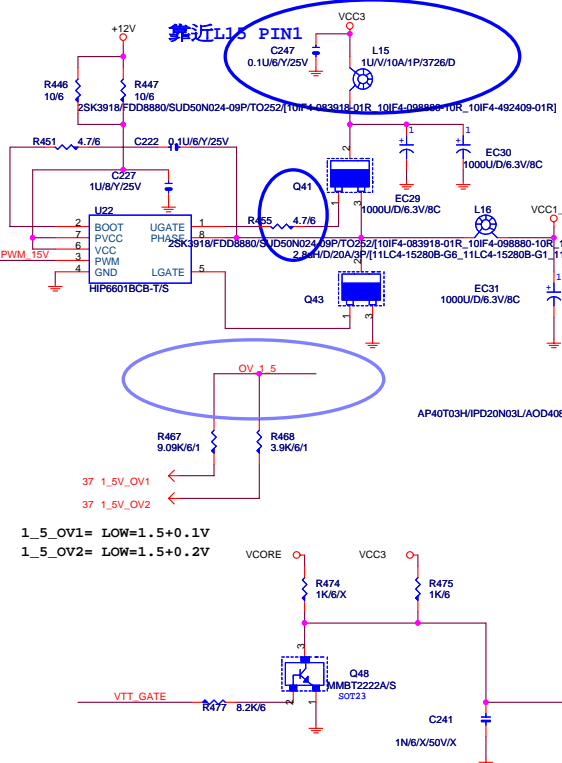




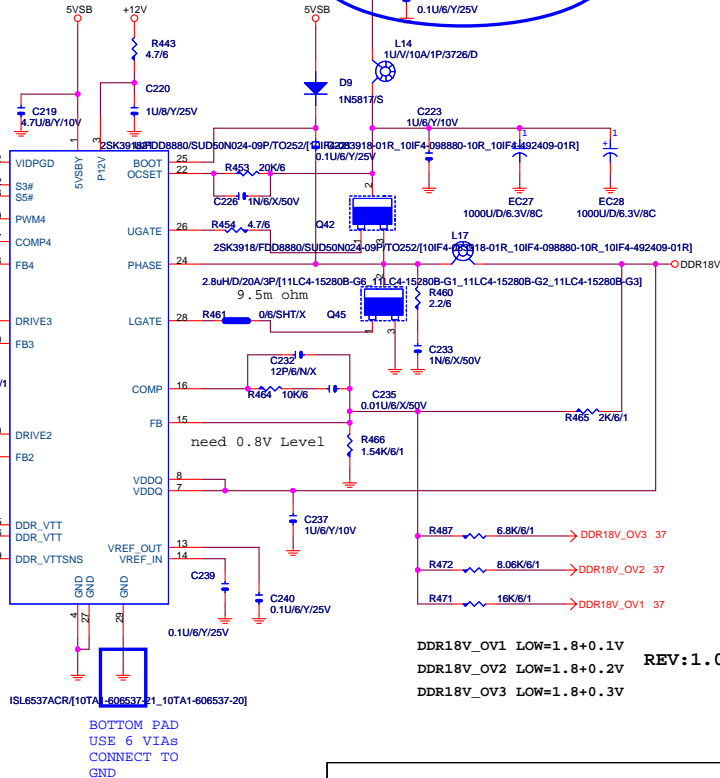
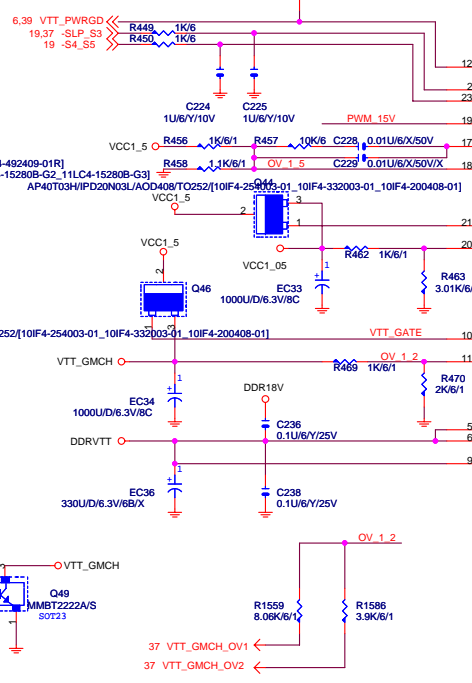




	OV1	OV2	OV3
1.8V	X	X	X
1.9V	L	X	X
2.0V	X	L	X
2.1V	X	X	L
2.2V	L	X	L
2.3V	X	L	L
2.4V	L	L	L



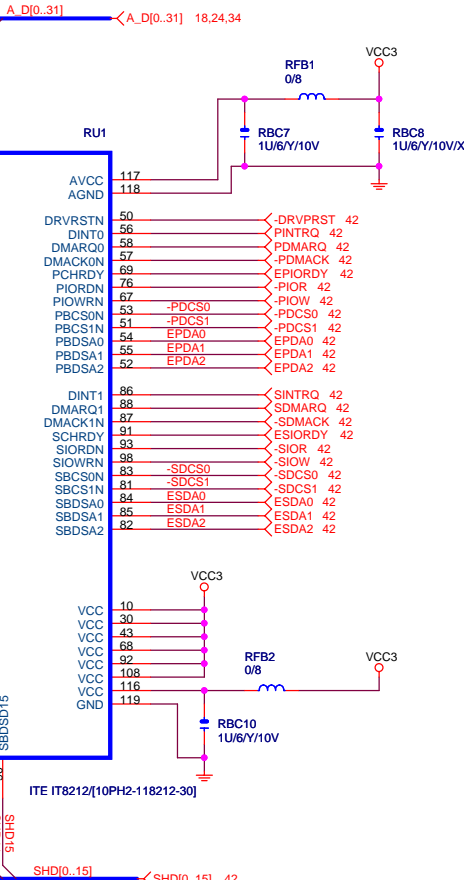
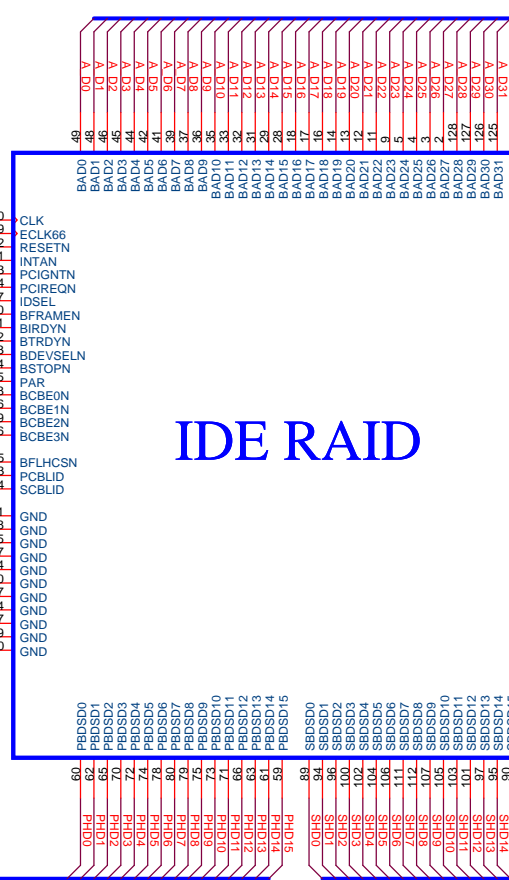
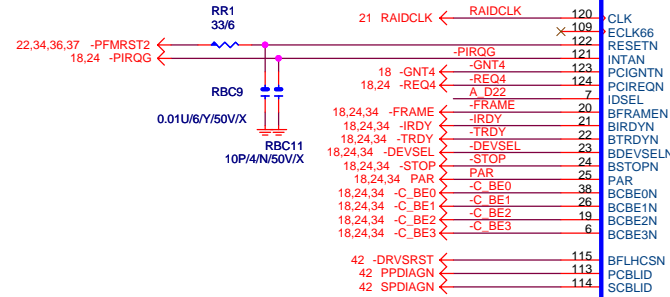
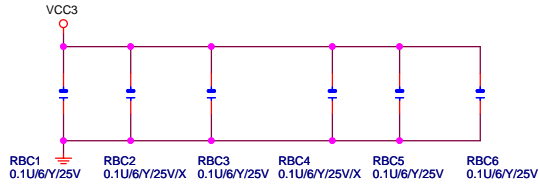
R402 Change to 1uF  
If ISL6537 pin12 Fail



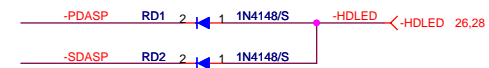
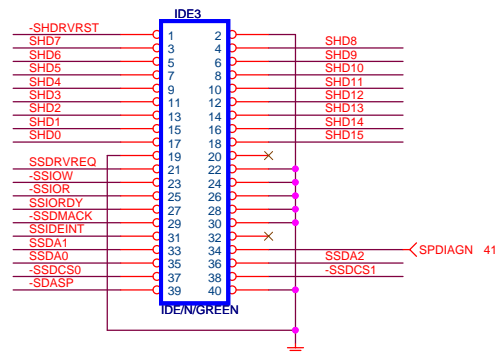
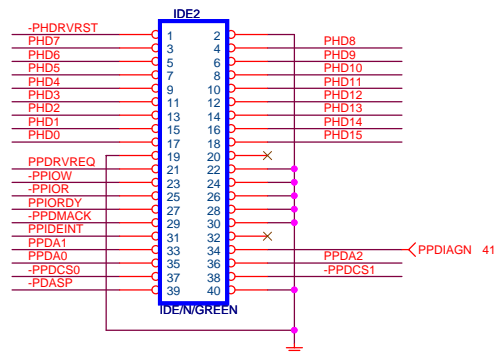
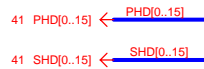
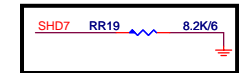
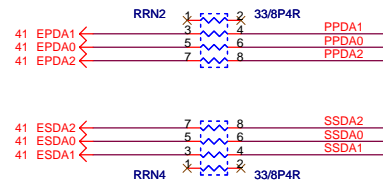
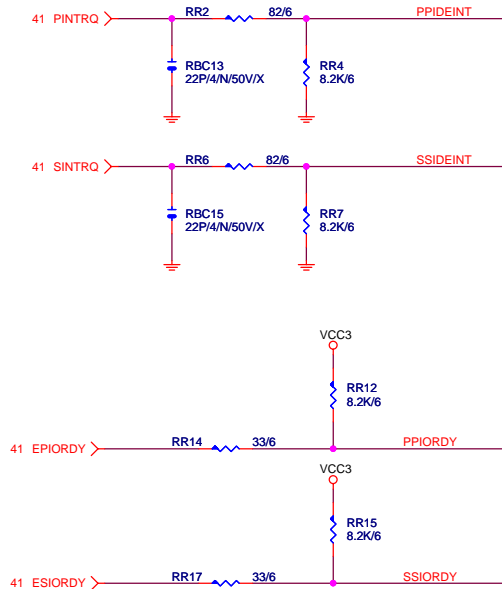
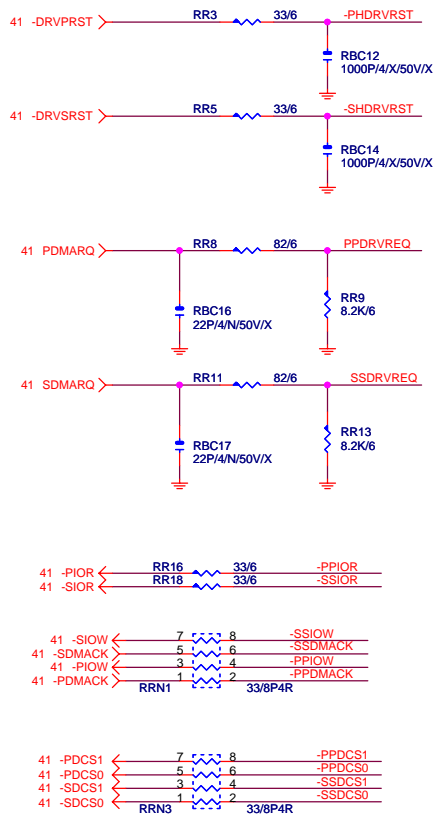
DDR18V\_OV1 LOW=1.8+0.1V  
DDR18V\_OV2 LOW=1.8+0.2V  
DDR18V\_OV3 LOW=1.8+0.3V



ALL INPUT PIN MUST HAVE 0.1 CAPACITOR



IDE RAID



*GPI PIN*

Pin Name	Pin Type	Power Well			GPIO Application
GPIO[0]	I/O	VCC3	GPI/BM_BUSY#	(NA)	(NA)
GPIO[1]	I/O	VCC	-REQ[5]	(P.U VCC)	-REQ[5]
GPIO[5:2]	I/OD	VCC	-PIRQ[H:E]	(P.U VCC)	-PIRQ[H:E]
GPIO[6]	I/O	VCC3	GPI	(NA)	M_ID0 FOR MEDIA
GPIO[7]	I/O	VCC3	GPI	(NA)	DUALBIOS_INPUT
GPIO[8]	I/O	3VDUAL	GPI	(P.U 3VDUAL)	-SKTOCC
GPIO[9]	I/O	3VDUAL	GPI	(NA)	P66DET
GPIO[10]	I/O	3VDUAL	GPI	(NA)	M_ID1 FOR MB_ID
GPIO[11]	I/O	3VDUAL	-SMBALERT	(P.U 3VDUAL)	-SMBALRT
GPIO[12]	I/O	3VDUAL	GPI	(NA)	M_ID2 FOR MB_ID
GPIO[13]	I/O	3VDUAL	GPI	(P.U 3VDUAL)	-LPCPME
GPIO[14]	I/O	3VDUAL	GPI	(NA)	M_ID3 FOR MB_ID
GPIO[15]	I/O	3VDUAL	GPI	(NA)	-ACZ_DET
GPIO[16]	I/O	VCC3	GPO	P.D 20K(INT.)	HW RESET
GPIO[17]	I/O	VCC3	GPO/-GNT[5]	(NA)	GPO/-GNT[5]
GPIO[18]	I/O	VCC3	GPO/toggle	(NA)	(NA)
GPIO[19]	I/O	VCC3	SATA1GP	(P.U VCC3)	SATA1GP
GPIO[20]	I/O	VCC3	GPO	(P.U VCC3)	TBL-
GPIO[21]	I/O	VCC3	SATA0GP	(P.U VCC3)	SATA0GP
GPIO[22]	I/O	VCC3	-REQ[4]	(P.U VCC)	-REQ[4]
GPIO[23]	I/O	VCC3	LDRQ1#	(NA)	(NA)
GPIO[24]	I/O	3VDUAL	GPO/reset not cleared	(NA)	(NA)
GPIO[25]	I/O	3VDUAL	GPO	(NA)	PWD_LED
GPIO[26]	I/O	3VDUAL	EL_RSVD	(P.D)	-SPI_WP
GPIO[27]	I/O	3VDUAL	EL_STATE0	(NA)	(NA)
GPIO[28]	I/O	3VDUAL	EL_STATE1	(NA)	(NA)
GPIO[29]	I/O	3VDUAL	OC5#	(P.U VCC 分壓)	OC5#
GPIO[30]	I/O	3VDUAL	OC6#	(P.U VCC 分壓)	OC6#
GPIO[31]	I/O	3VDUAL	OC7#	(P.U VCC 分壓)	OC7#
GPIO[32]	I/O	VCC3	GPO	(NA)	DUAL_BIOS
GPIO[33]	I/O	VCC3	GPO	(NA)	DUAL_BIOS
GPIO[34]	I/O	VCC3	GPO	(P.U VCC3)	FWP-
GPIO[35]	I/O	VCC3	SATACLKREQ#	(NA)	(NA)
GPIO[36]	I	VCC3	SATA2GP	(P.U VCC3)	SATA2GP
GPIO[37]	I	VCC3	SATA3GP	(P.U VCC3)	SATA3GP

*GPO PIN*

Pin Name	Pin Number	Power Well	Pin Type		GPIO Application
GPIO[38]	I/O	VCC3	GPI	(NA)	(NA)
GPIO[39]	I/O	VCC3	GPI	(NA)	(NA)
GPIO[40:47]		NOT IMPLEMENTED		NOT IMPLEMENTED	
GPIO[48]	I/O	VCC3	-GNT[4]	(NA)	-GNT[4]
GPIO[49]	I/O	VTT_GMCH	CPUPWRGD	(P.U VTT_OL)	CPUPWROK
PCI1	PCLK0	-PCIRST	-REQ0/-GNT0	-PIRQE	A_D16
PCI2	PCLK1	-PCIRST	-REQ1/-GNT1	-PIRQD	A_D17
PCI3	PCLK2	-PCIRST	-REQ2/-GNT2	-PIRQC	A_D18
1394b	1394CLK	-PFMRST2	-REQ3/-GNT3	-PIRQH	A_D23
IT8212	RAIDCLK	-PFMRST2	-REQ4/-GNT4	-PIRQG	A_D22

ICH6 GPIO Table:

NAME	PWR LANE	USAGE	NAME	PWR LANE	USAGE
GPI0	V5REF	M/B ID (-REQ6)	GPI41	VCC3	M/B ID
GPI1	V5REF	-REQ5	GPO48	VCC3	-GNT4
GPI2	V5REF	-PIRQE	GPO49	V-CPUIO	CPUPWOK
GPI3	V5REF	-PIRQF			
GPI4	V5REF	-PIRQG			
GPI5	V5REF	-PIRQH			
GPI6	VCC3	-SLP BTN			
GPI7	VCC3	DUAL BIOS			
GPI8	3VDAUL	-LANWAKE			
GPI9	3VDAUL	-USBOC4			
GPI10	3VDAUL	-USBOC5			
GPI11	3VDAUL	-SMBALT			
GPI12	VCC3	ATX DET			
GPI13	3VDAUL	-LPCPME			
GPI14	3VDAUL	-USBOC6			
GPI15	3VDAUL	-USBOC7			
GPO16	VCC3	CPU OV1 (-GNT6)			
GPO17	VCC3	-GNT5			
GPO18	VCC3	CPU OV2			
GPO19	VCC3	DUAL BIOS			
GPO20	VCC3	BIOS T-BLOCK			
GPO21	VCC3	DUAL BIOS			
GPO23	VCC3	DDR OV0			
GPI024	3VDAUL	GREEN LED			
GPI025	3VDAUL	DDR OV1			
GPI26	VCC3	SATA GP0			
GPI027	3VDAUL	+PWRLED			
GPI028	3VDAUL	-PWRLED			
GPI29	VCC3	SATA GP1			
GPI30	VCC3	SATA GP2			
GPI31	VCC3	SATA GP3			
GPI032	VCC3	BIOS WP			
GPI033	VCC3	AZALIA DET			
GPI034	VCC3	PWRLED			
GPI40	V5REF	-REQ4			

PWROK/RESET Table:

ITE8712BHX PIN	NET NAME	TARGET
PIN62/-PCIRST1	-PCIE_RST	1. PCI-E * 1 Slot1 2. PCI-E * 1 Slot2 3. PCI-E * 1 Slot3 4. PCI-E * 16 Slot
PIN64/-PCIRST2	-PFMRST2	1. Onboard PCI Lan 2. Onboard 1394 Chip 3. OnBoard FWH
PIN65/-PCIRST3	-PFMRST1	1. Onboard PCI-E Lan 2. Onboard SATA Chip 3. GMCH
PIN115/-PCIRST4	-PFMRST_ -IDERST	Reserved For IDE
PIN63/PWROK1	PWROK1	1. GMCH 2. ICH6 3. 5VDUAL SWITCH 4. DPS CONTROL
PIN109/PWROK2	-THERM	1. ICH6

**GIGABYTE THCNOLOGIES , INC.**

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